

FIG. 1A

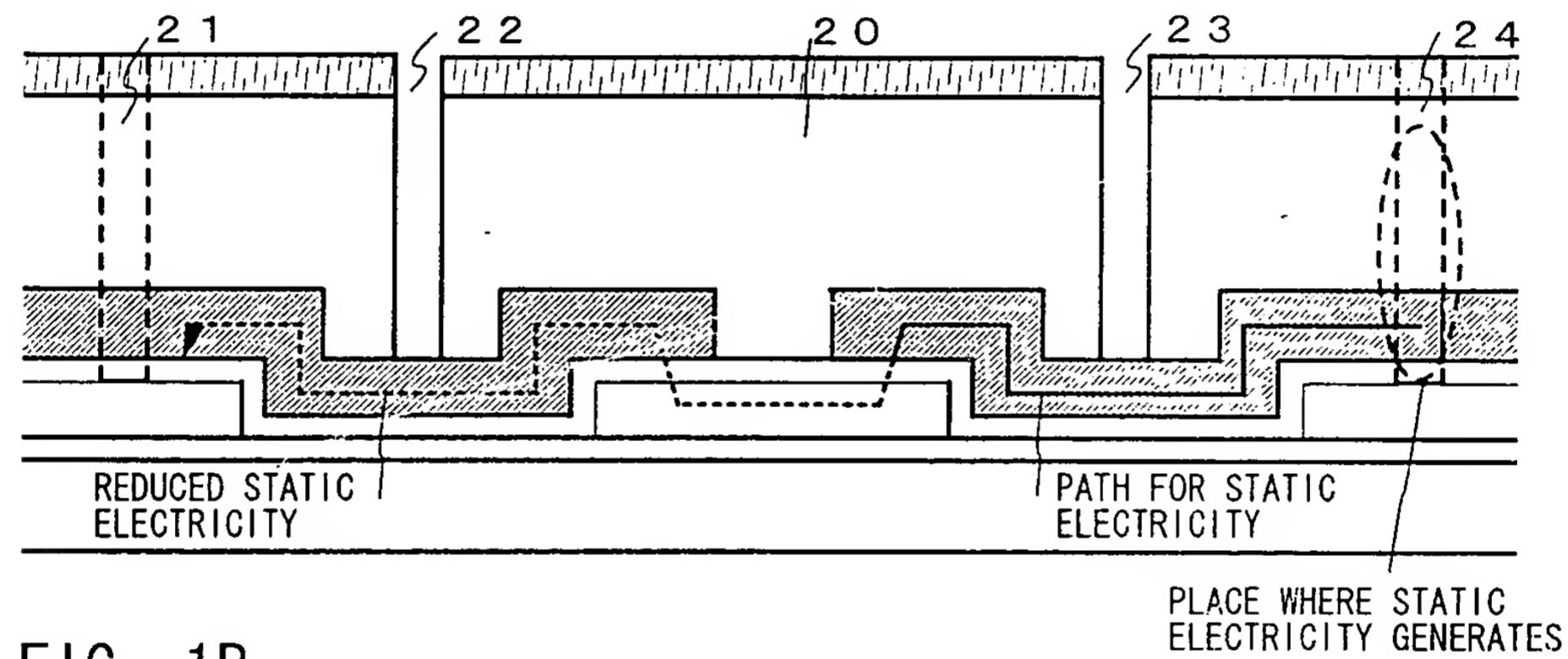


FIG. 1B

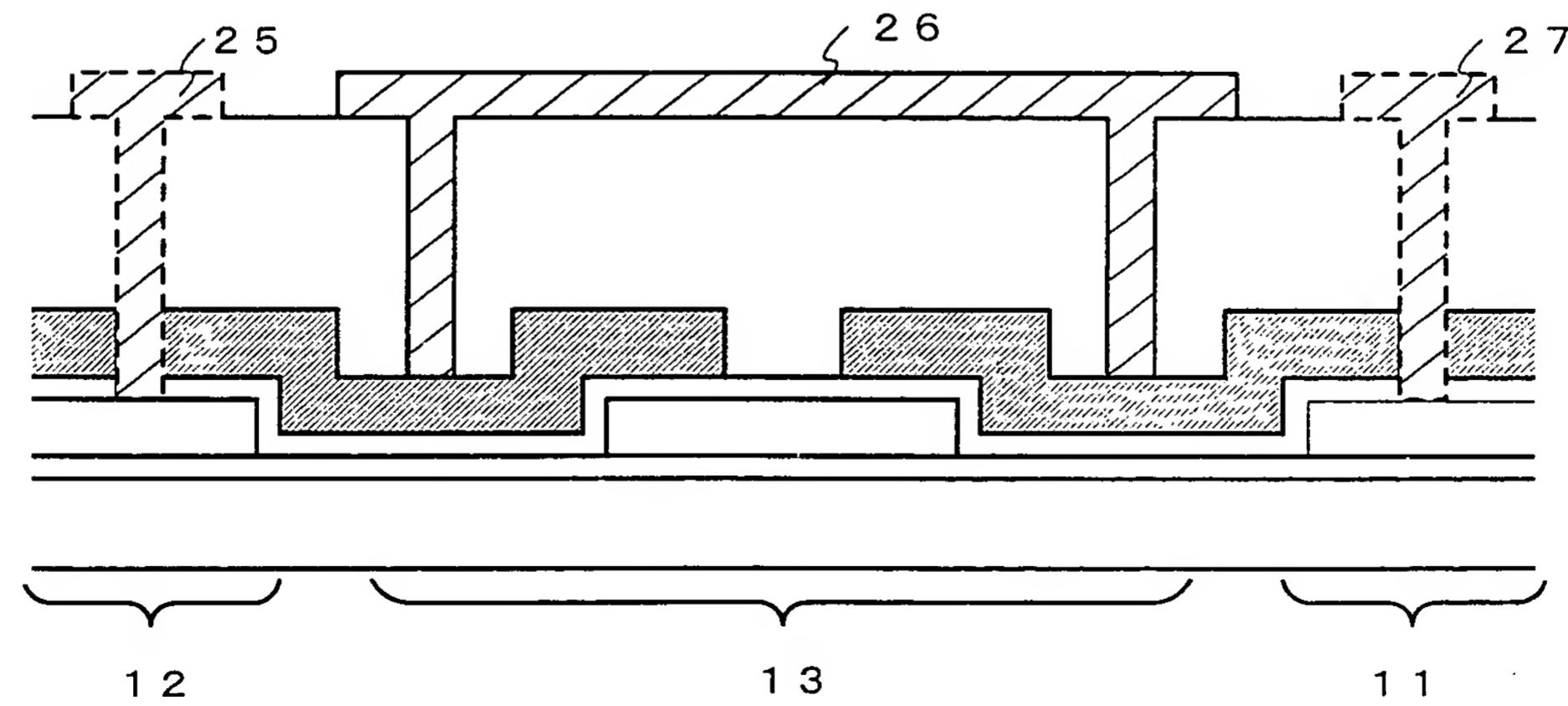


FIG. 1C

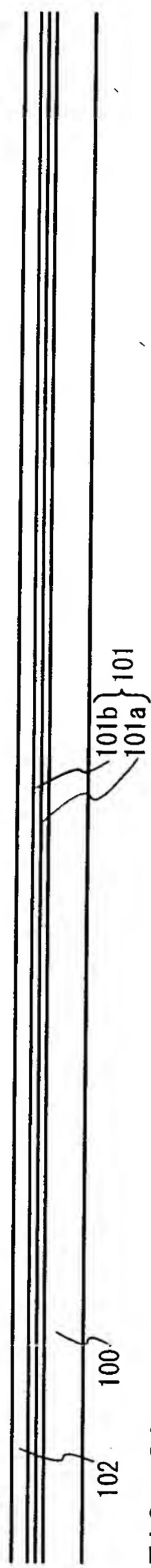


FIG. 2A



FIG. 2B

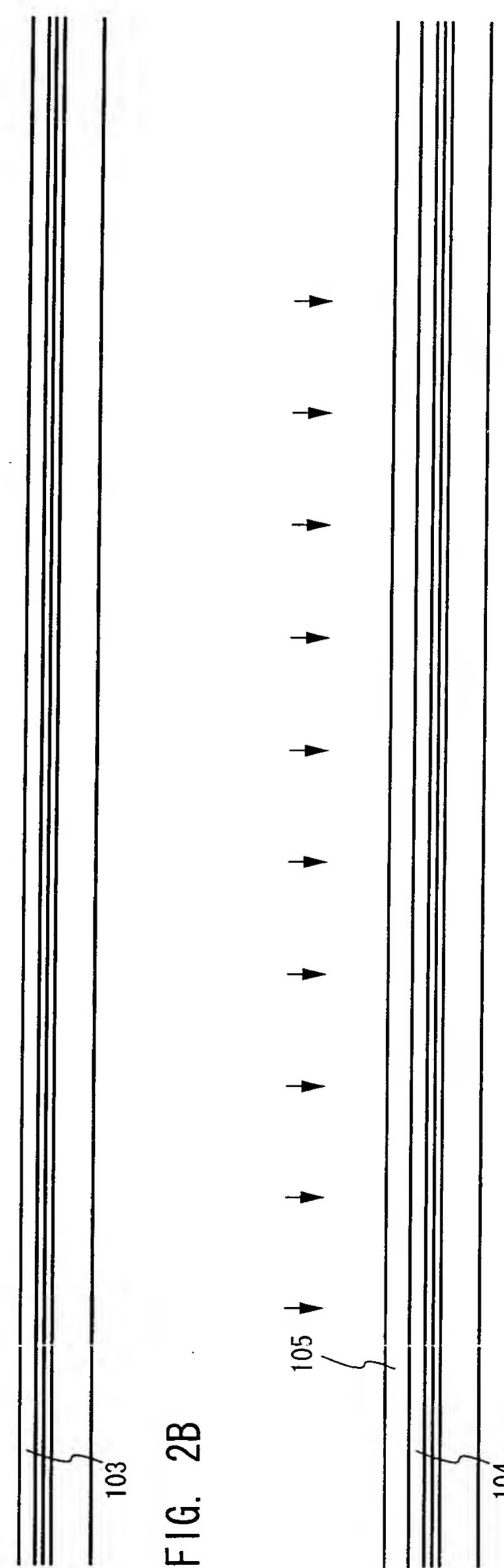


FIG. 2C

201 202 203 204

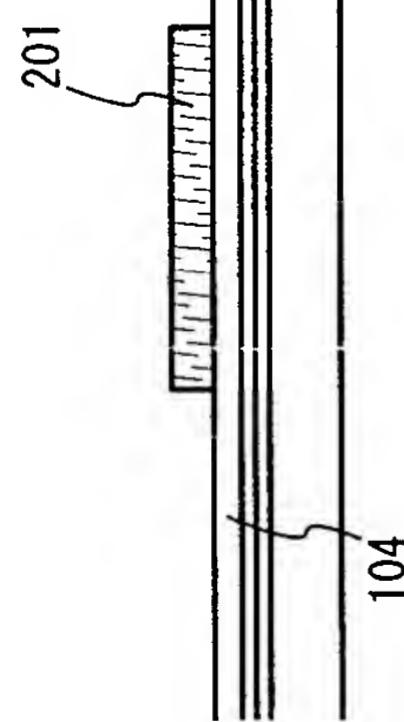


FIG. 3A

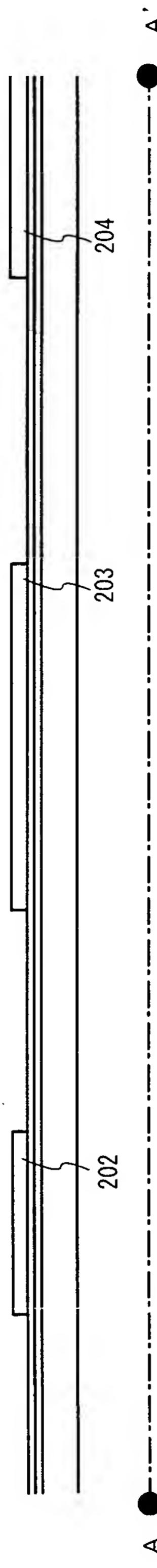


FIG. 3B

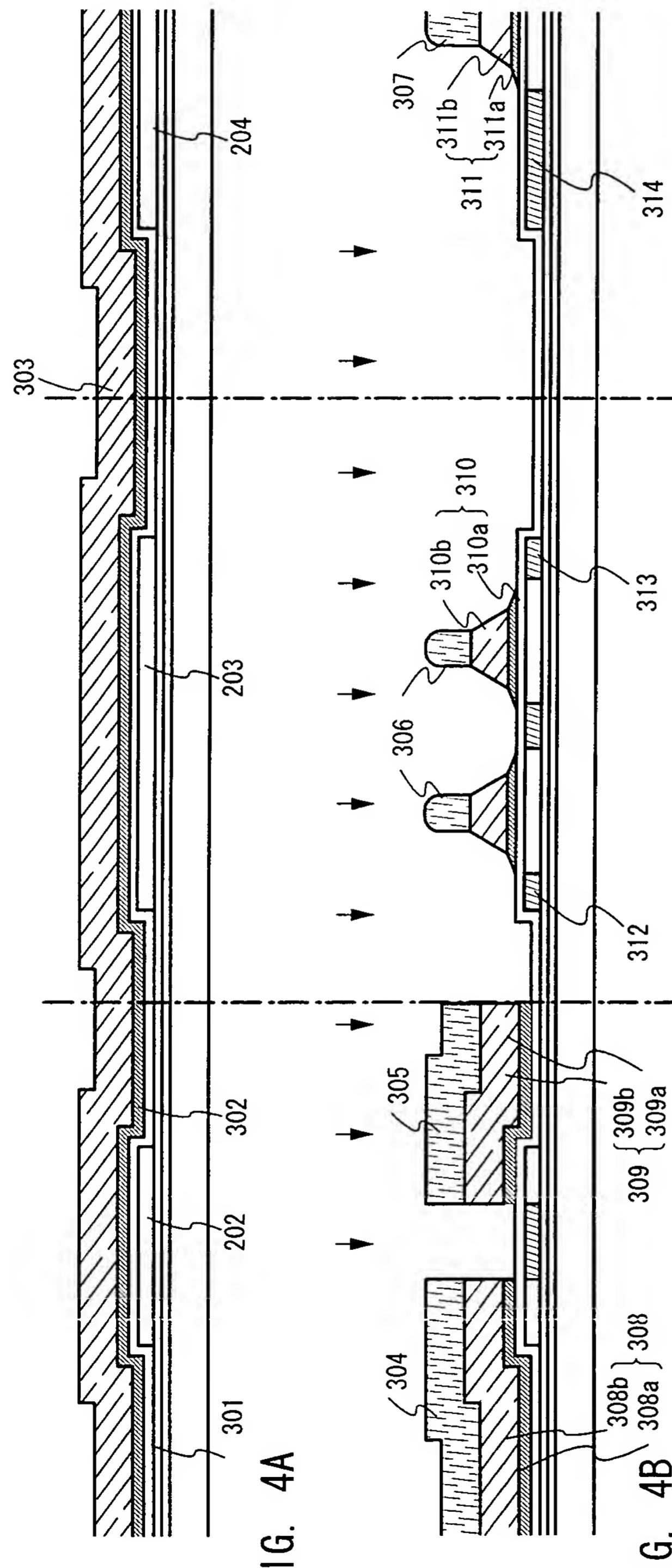


FIG. 4A

FIG. 4B

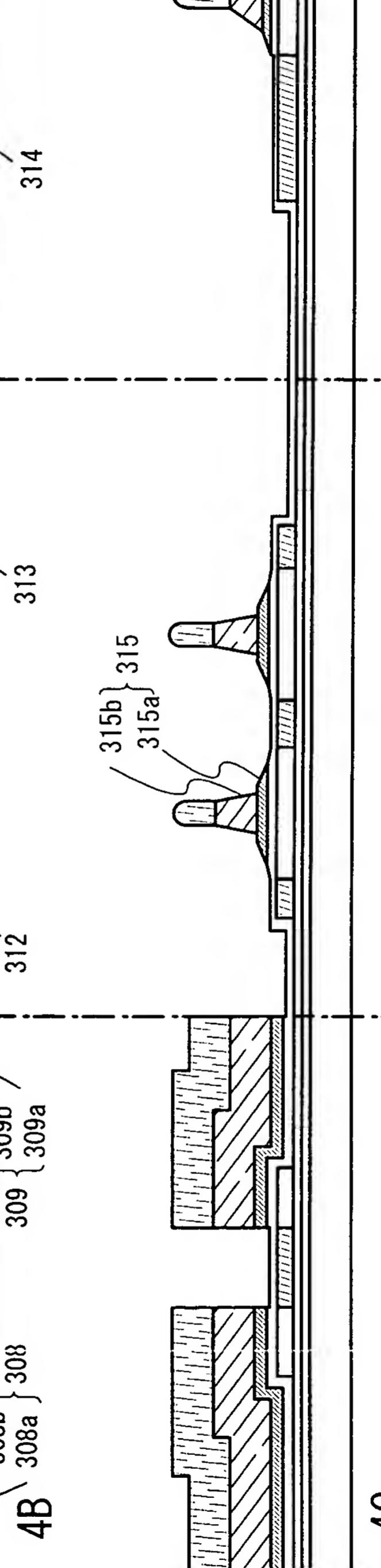


FIG. 4C

A A'

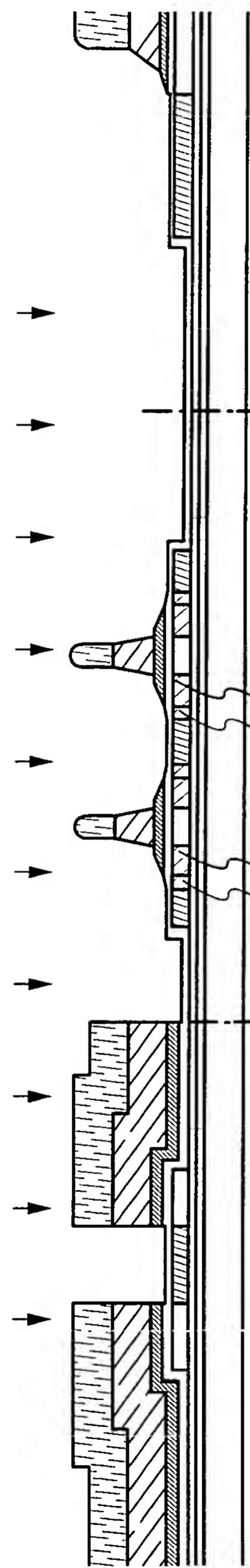


FIG. 5A

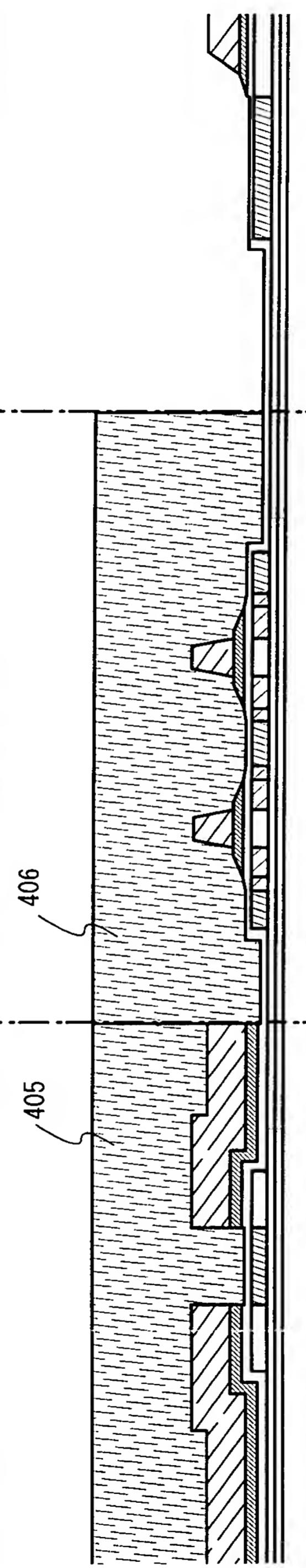


FIG. 5B

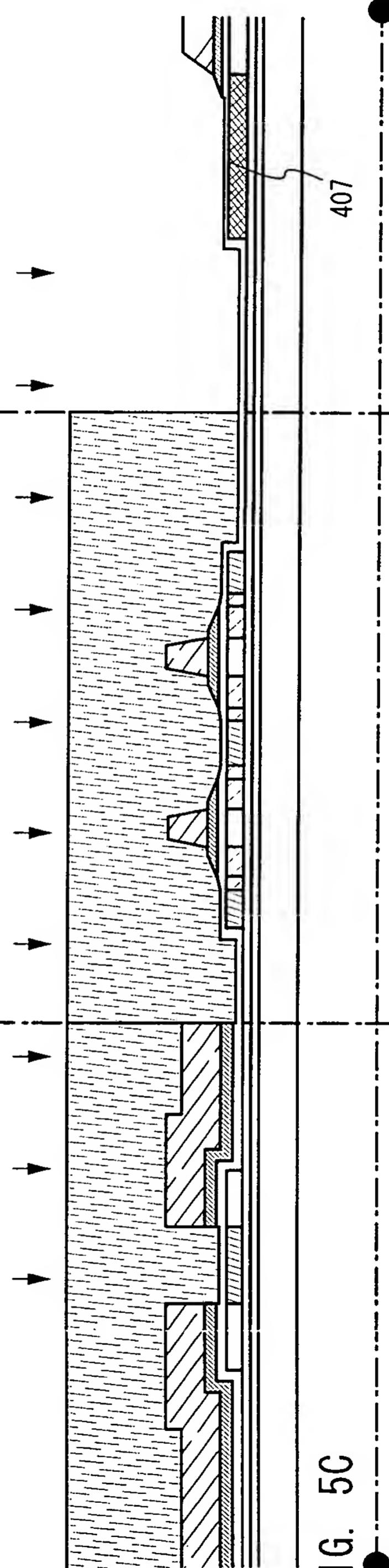


FIG. 5C

A'

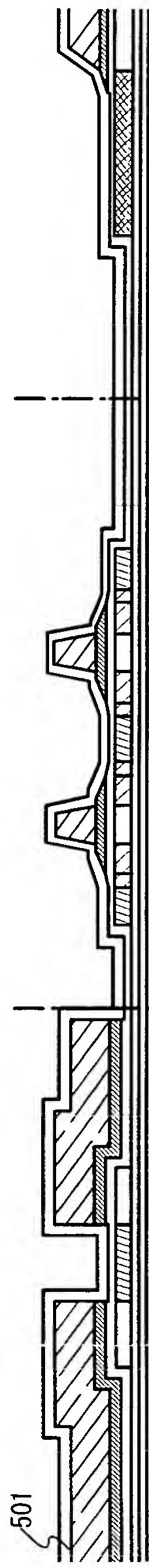


FIG. 6A

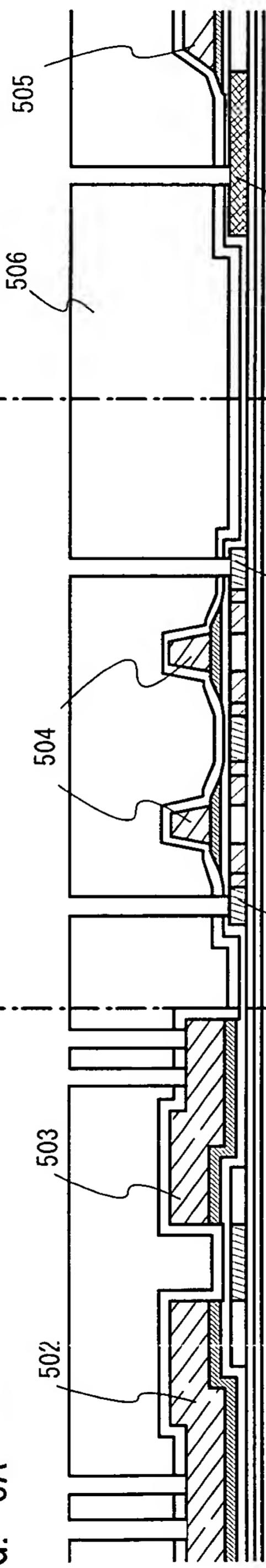
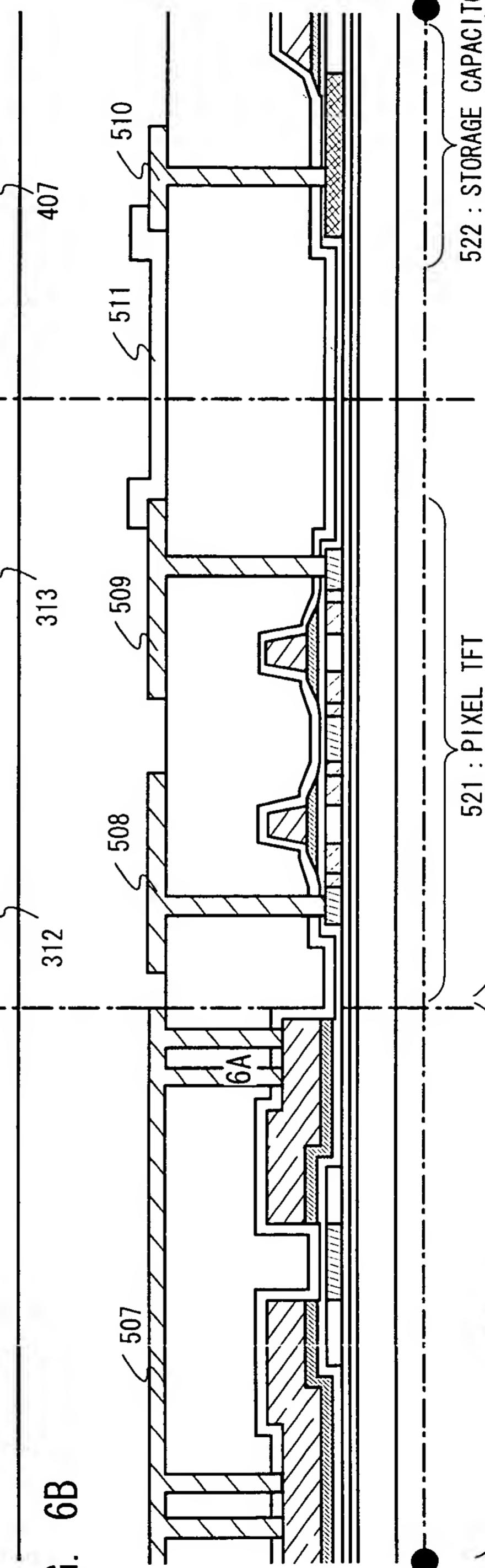


FIG. 6B



520 : FIRST PROTECTIVE CIRCUIT

521 : PIXEL TFT

522 : STORAGE CAPACITOR

523 : PIXEL REGION

A

FIG. 6C

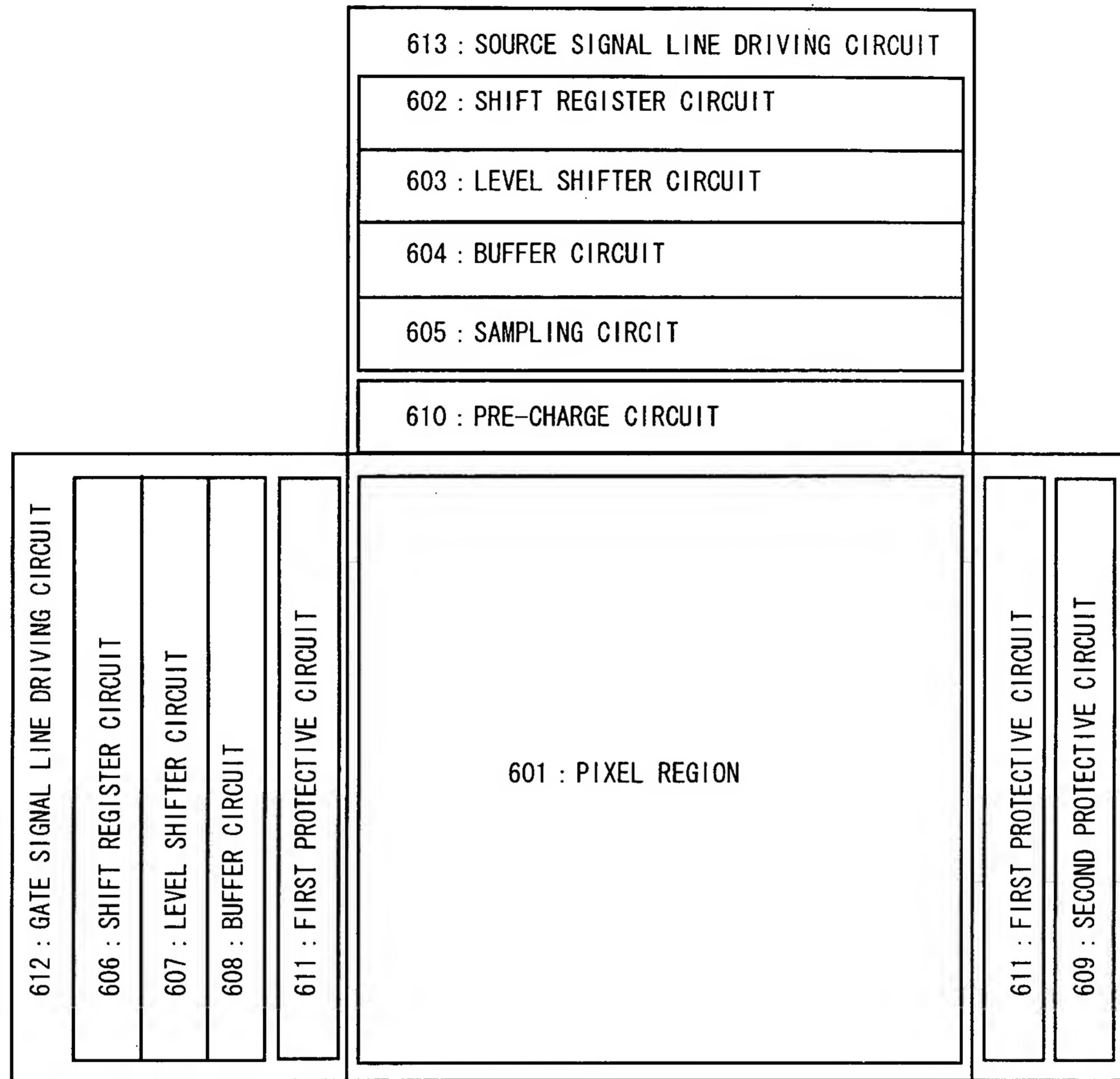


FIG. 7

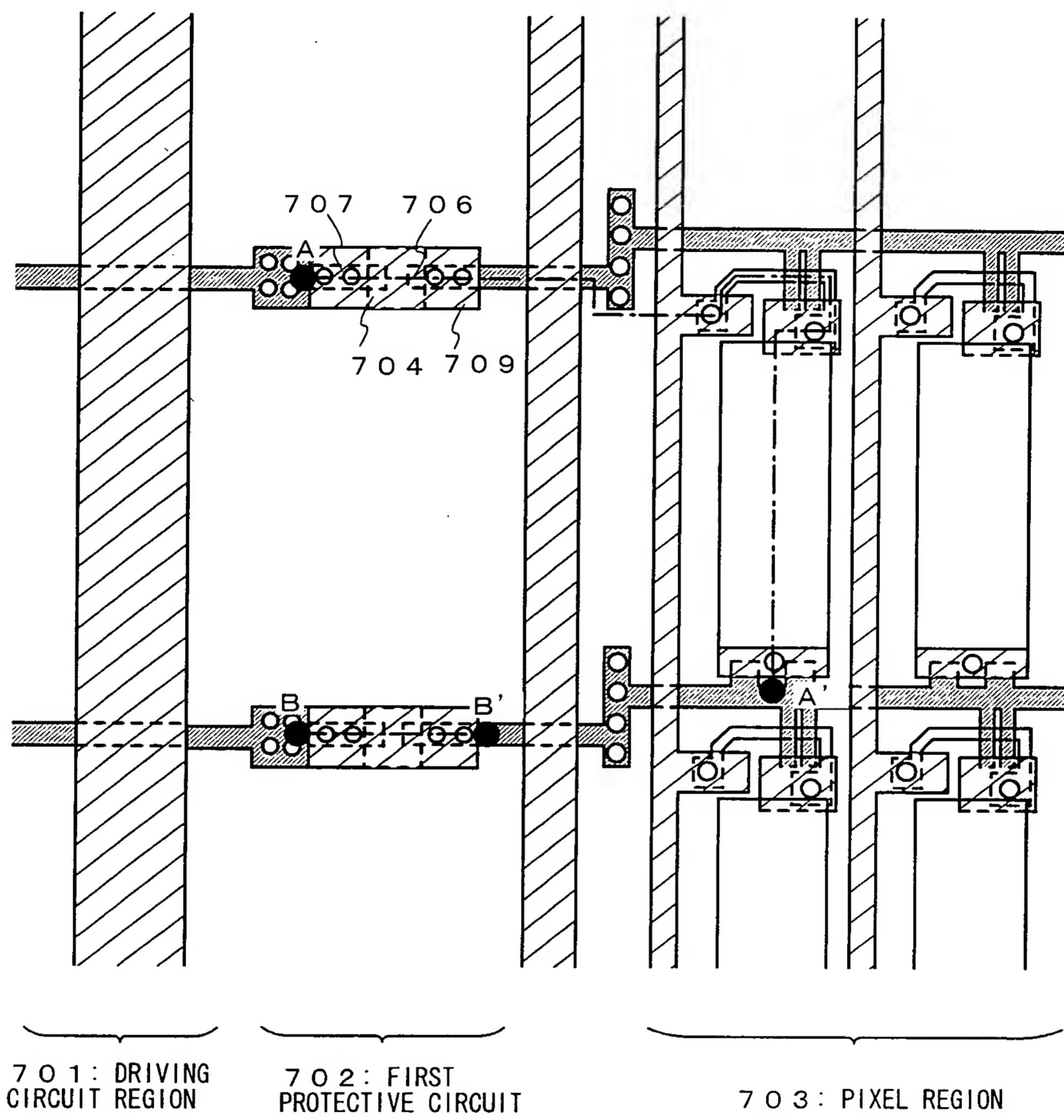


FIG. 8A

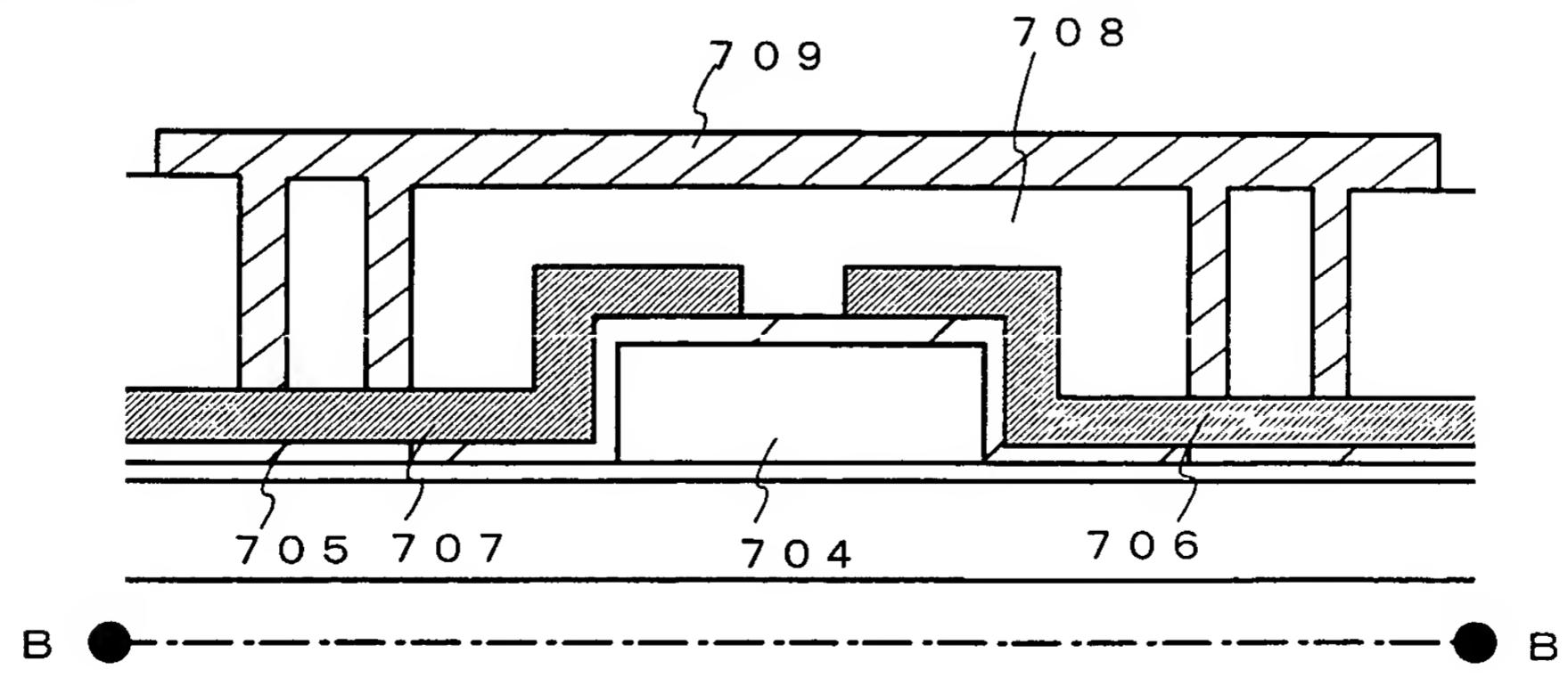


FIG. 8B

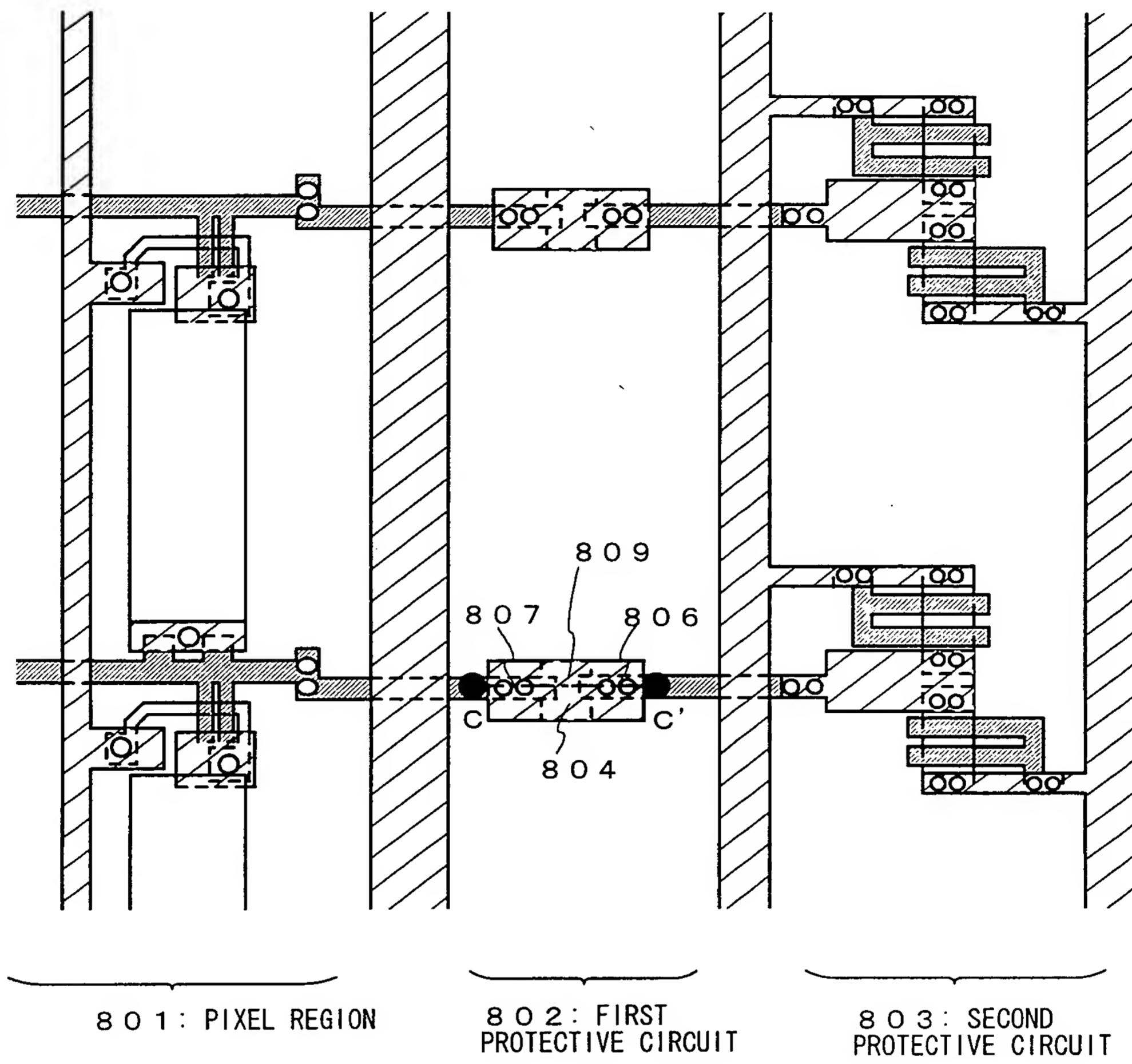


FIG. 9A

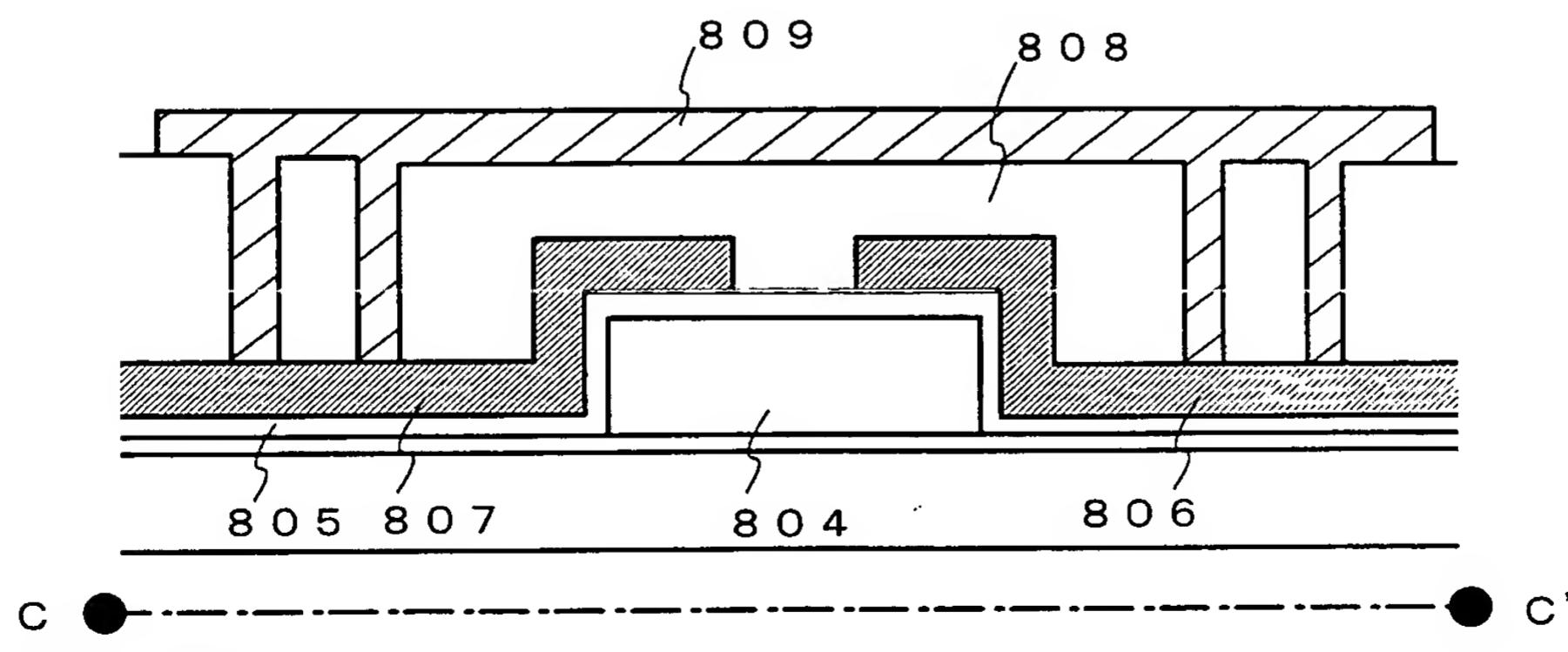


FIG. 9B

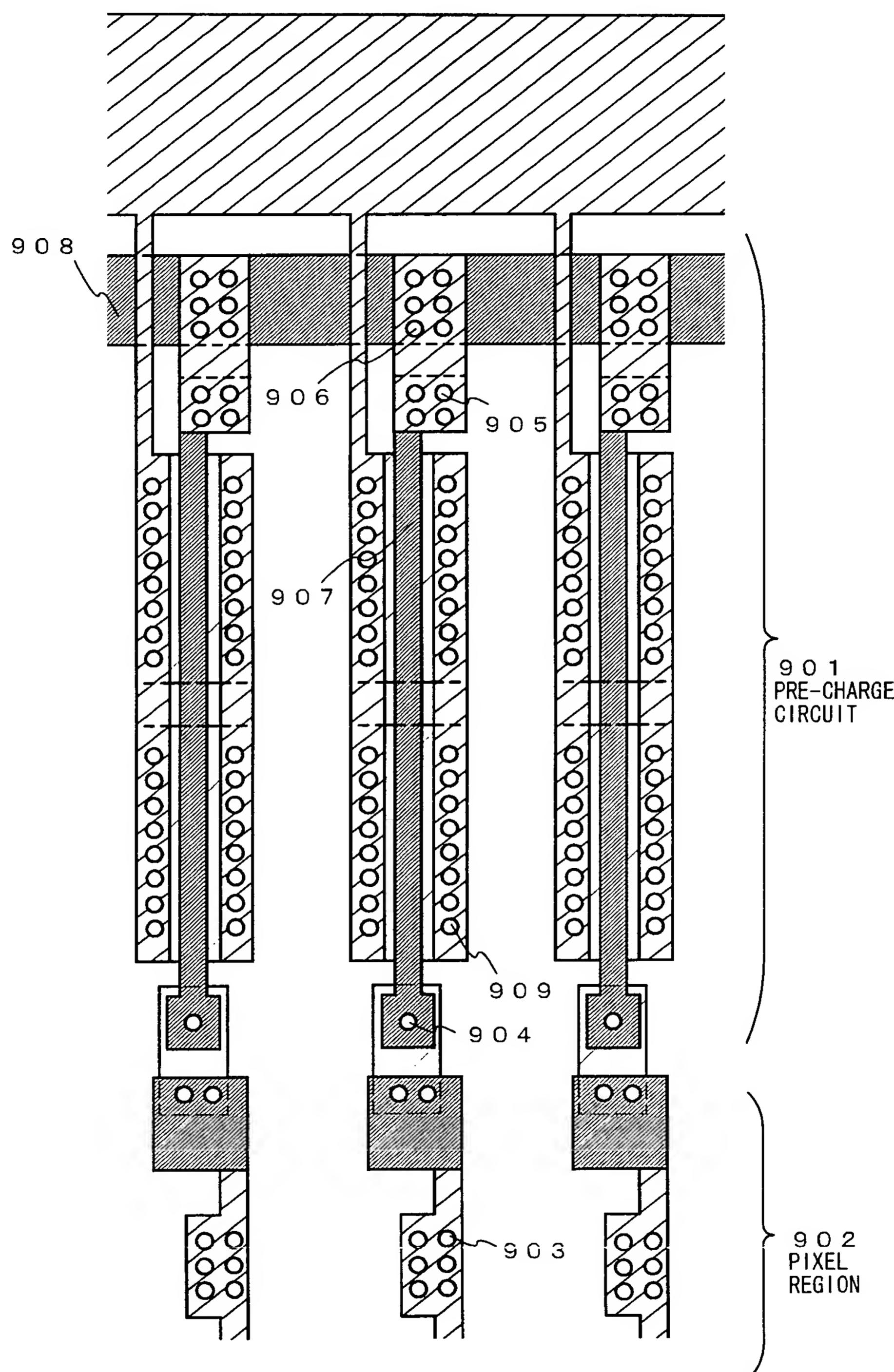


FIG. 10

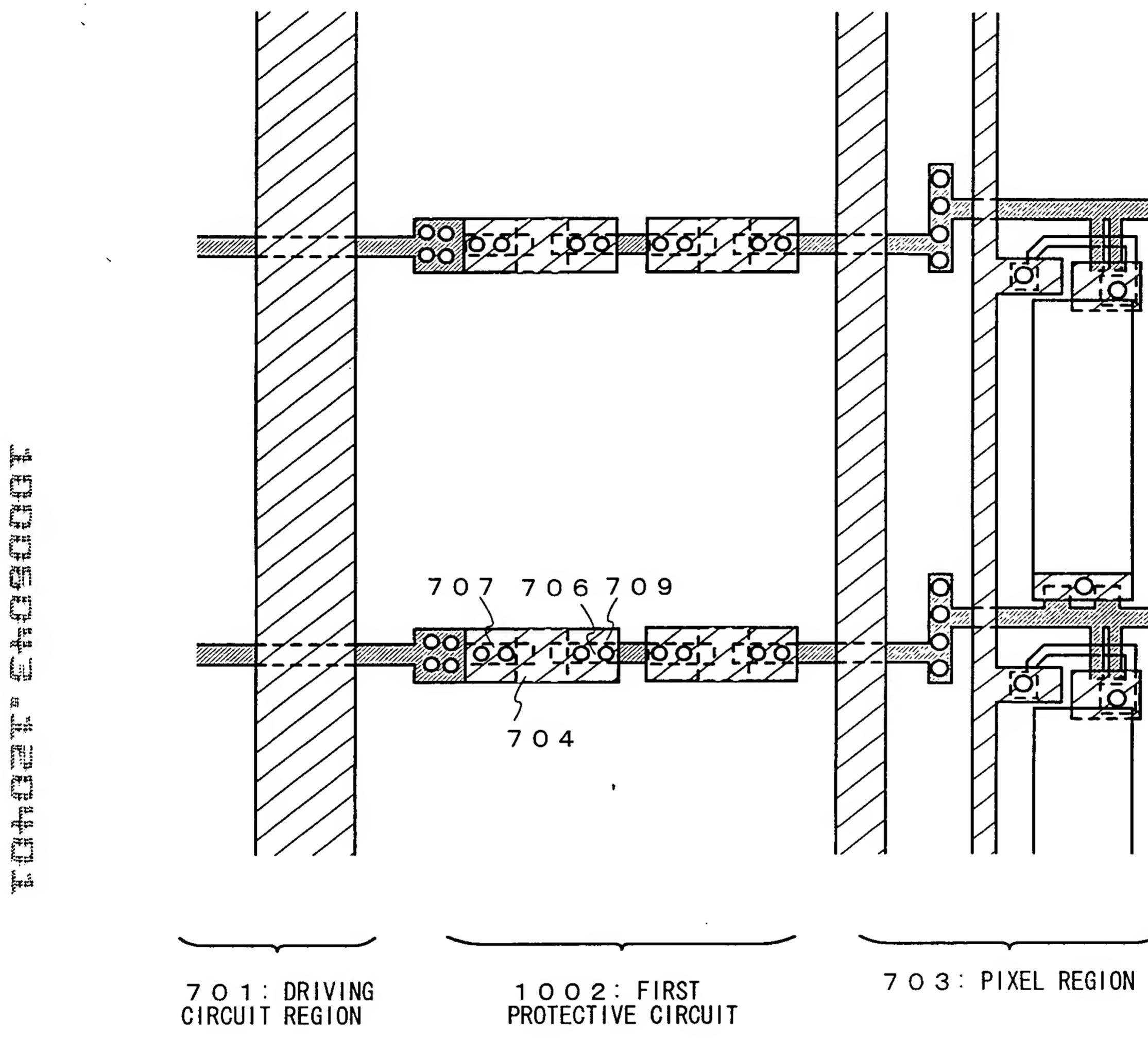


FIG. 11

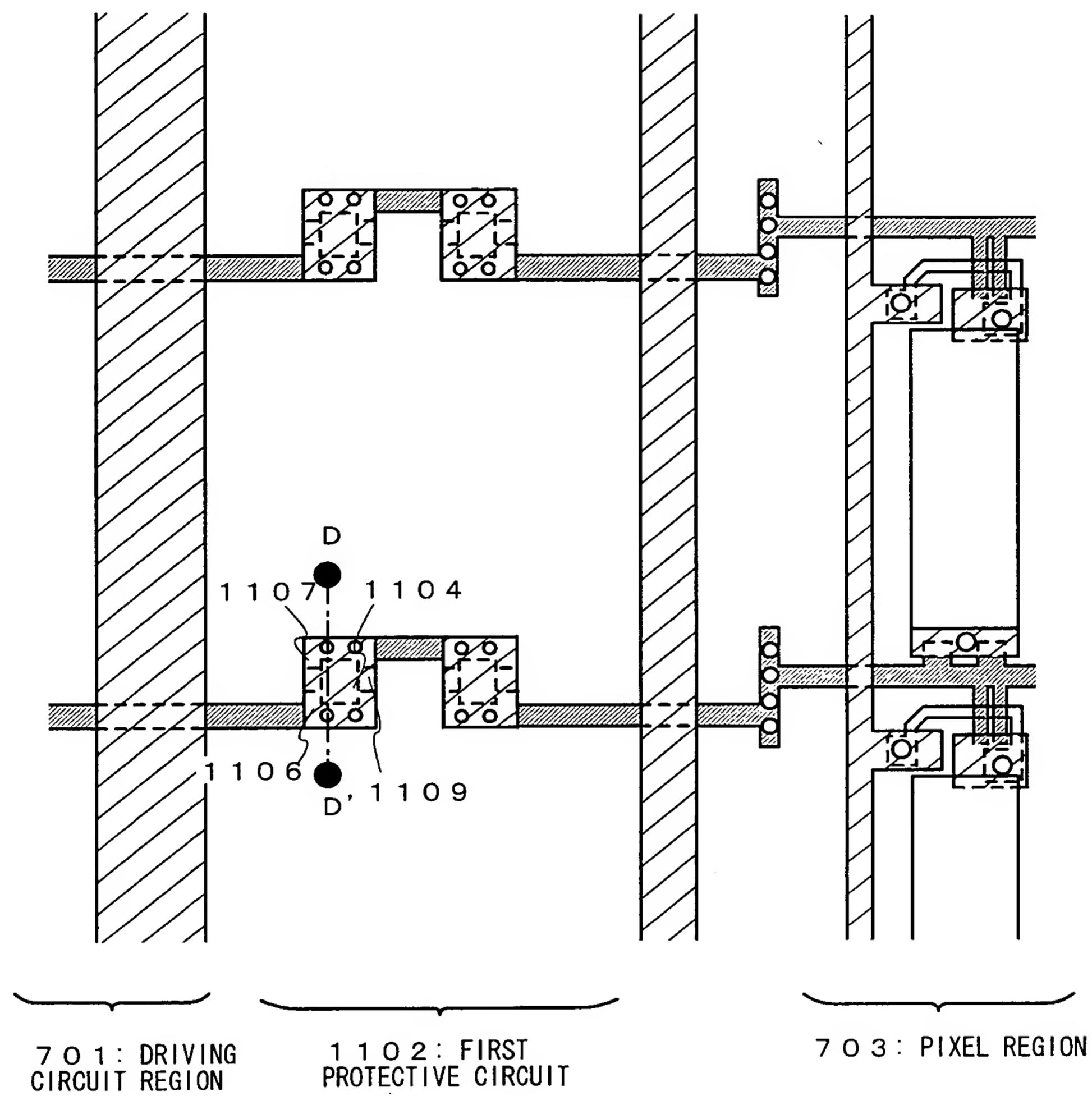


FIG. 12A

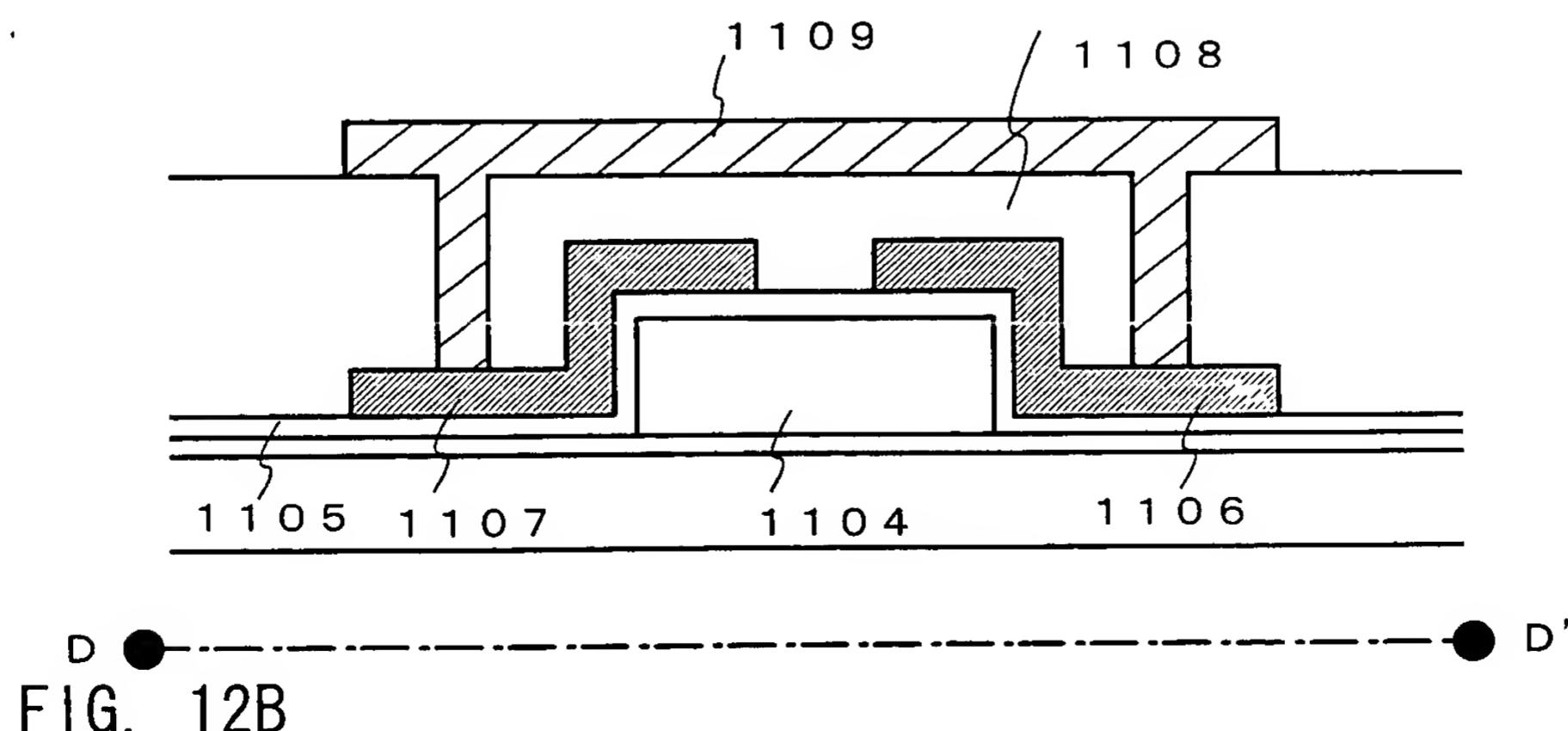


FIG. 12B

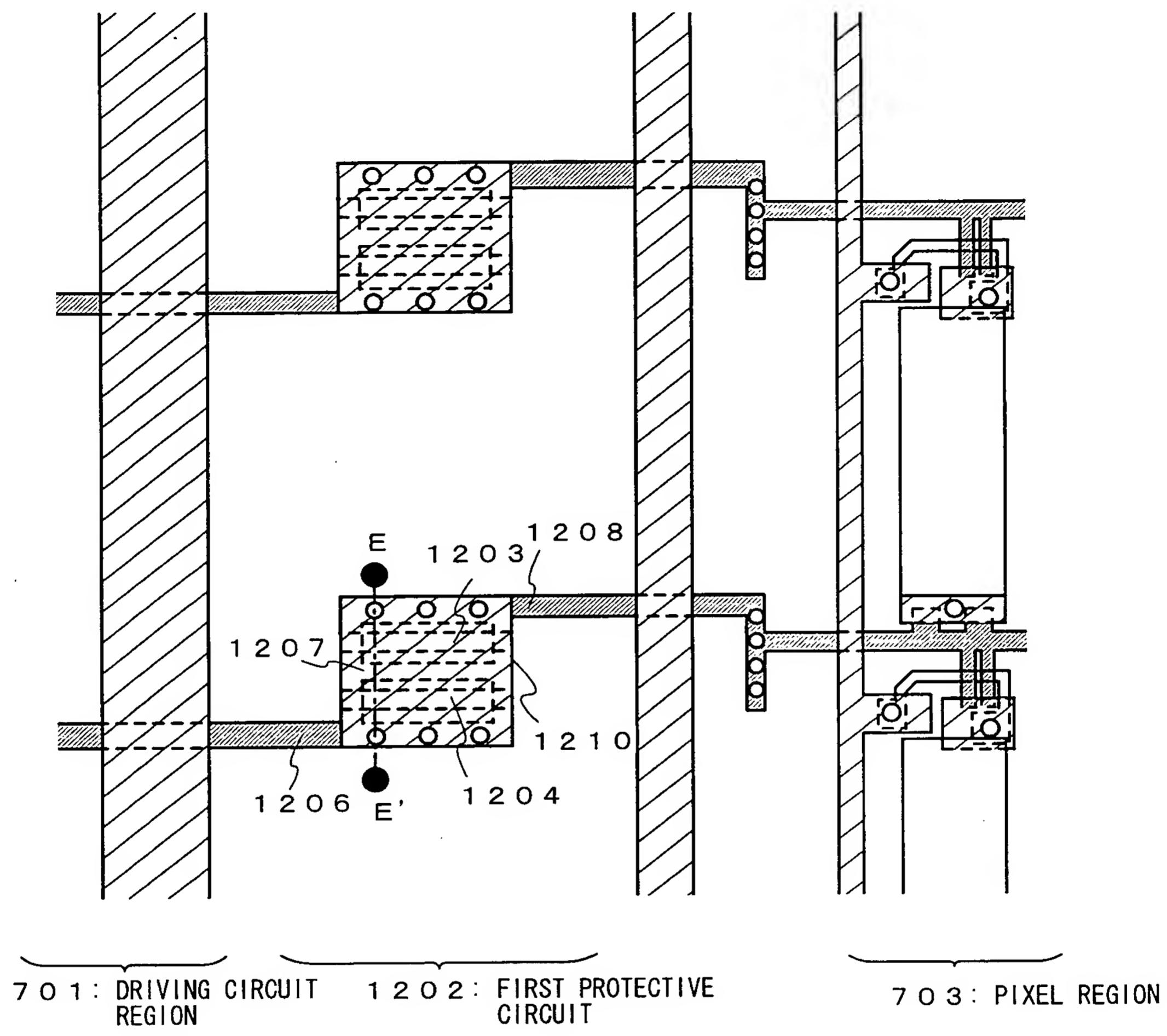


FIG. 13A

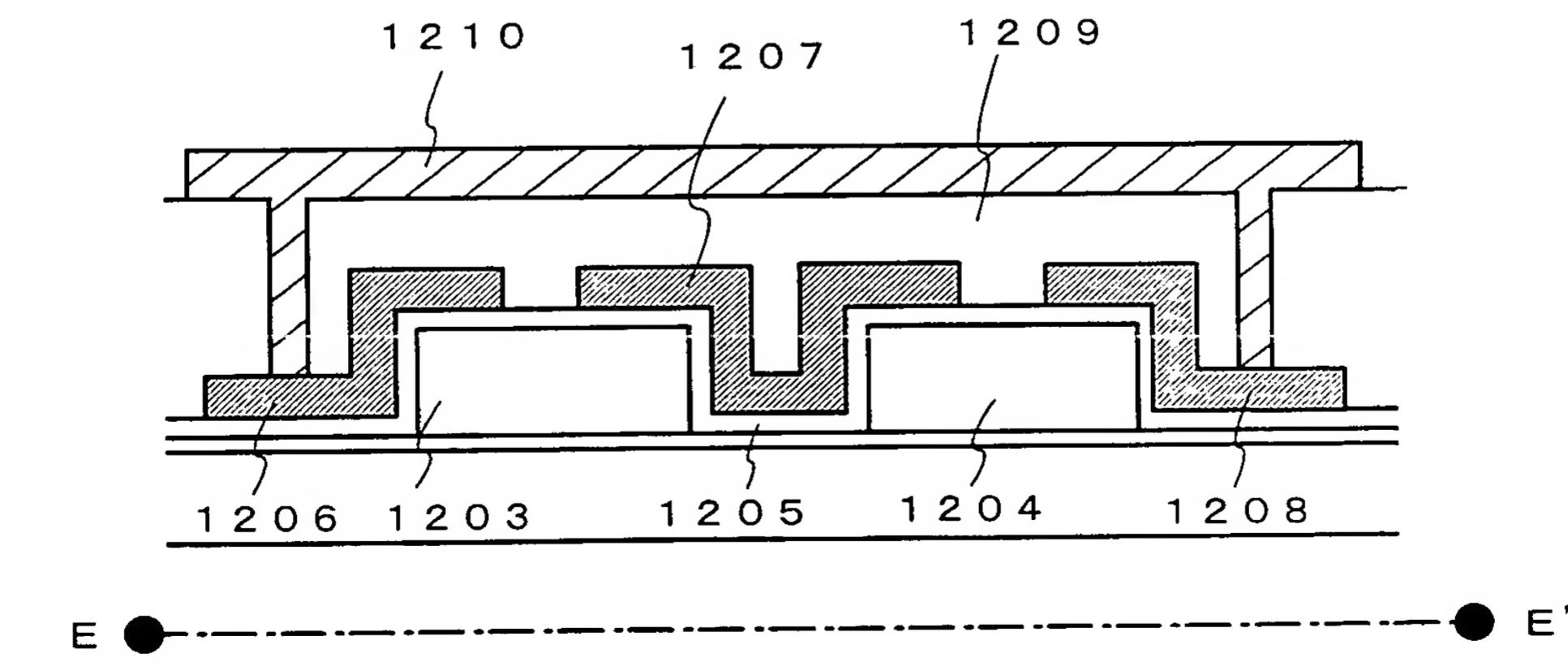


FIG. 13B

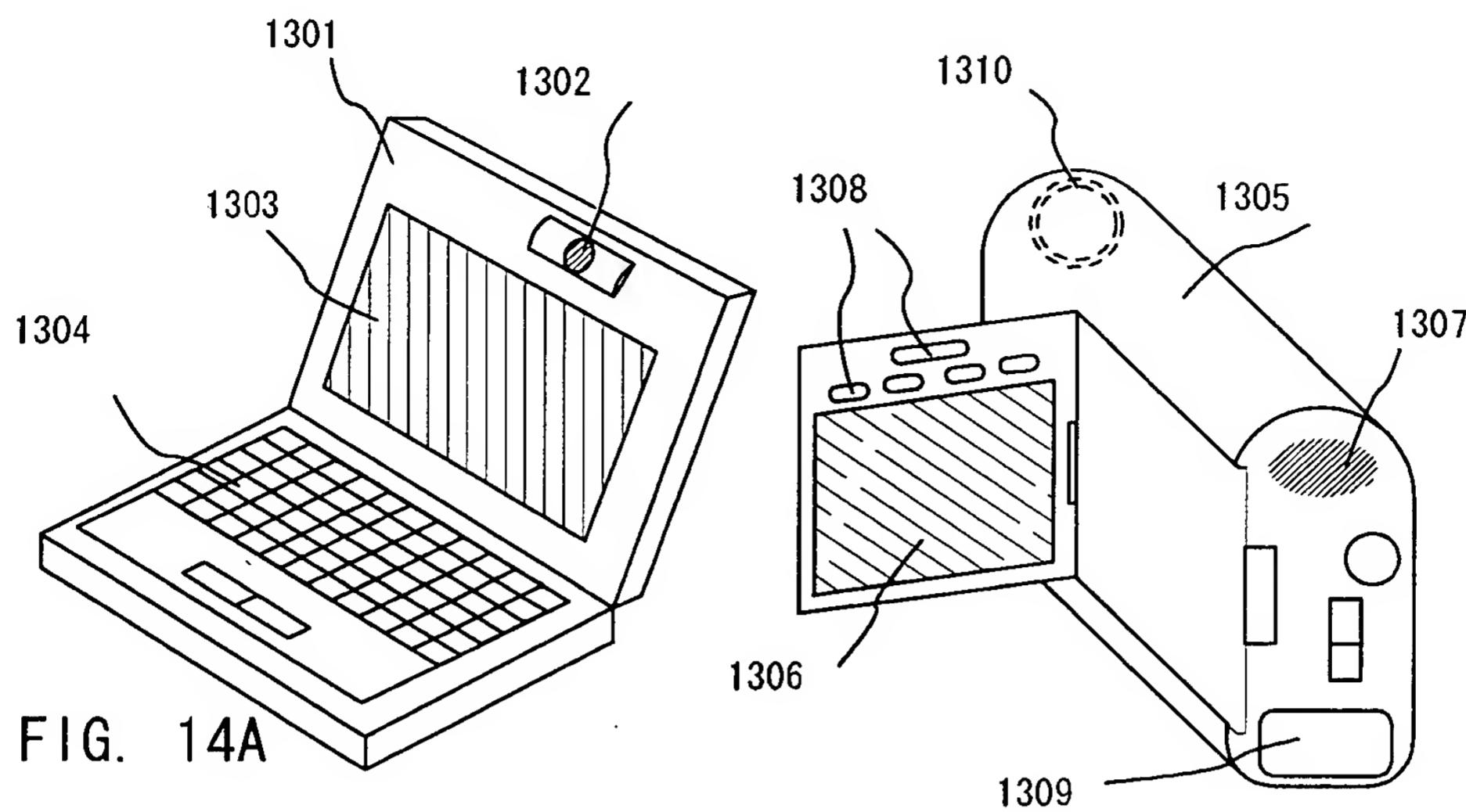


FIG. 14A

FIG. 14B

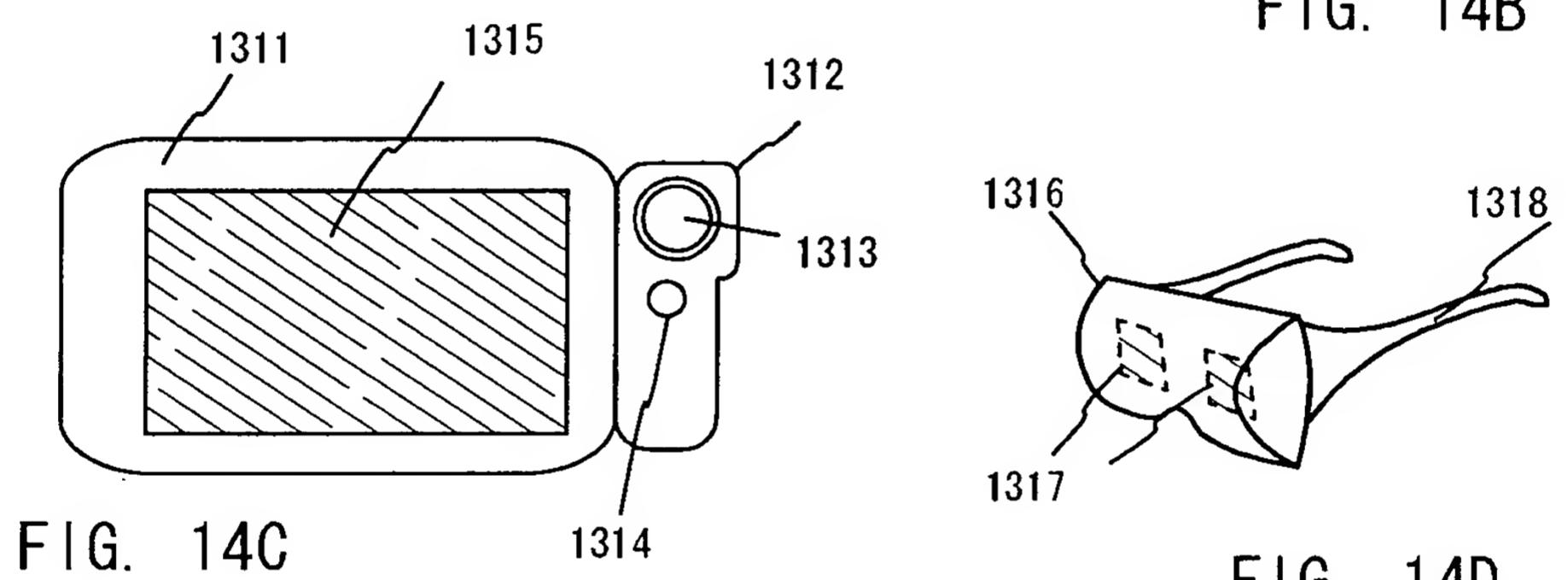


FIG. 14C

FIG. 14D

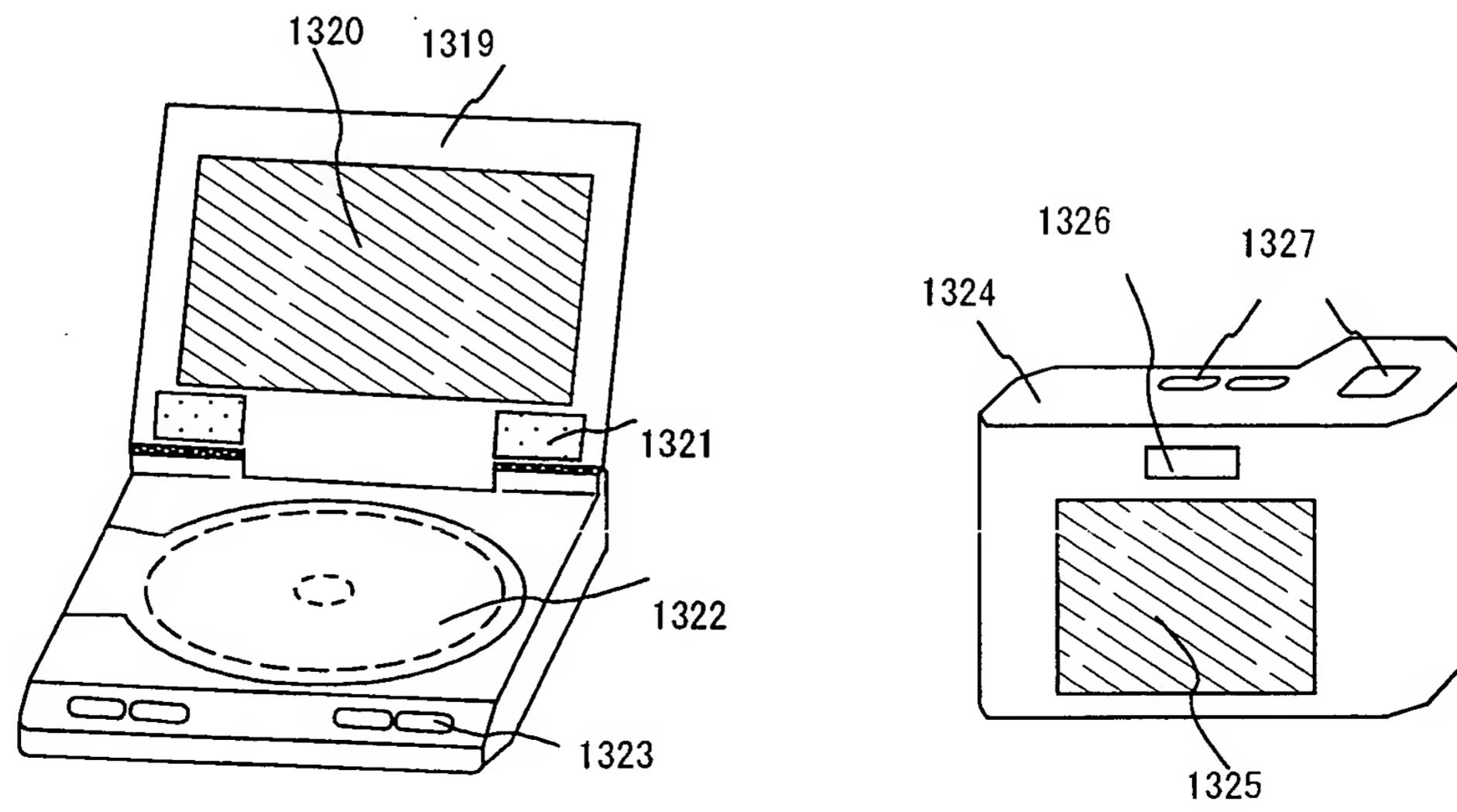


FIG. 14E

FIG. 14F

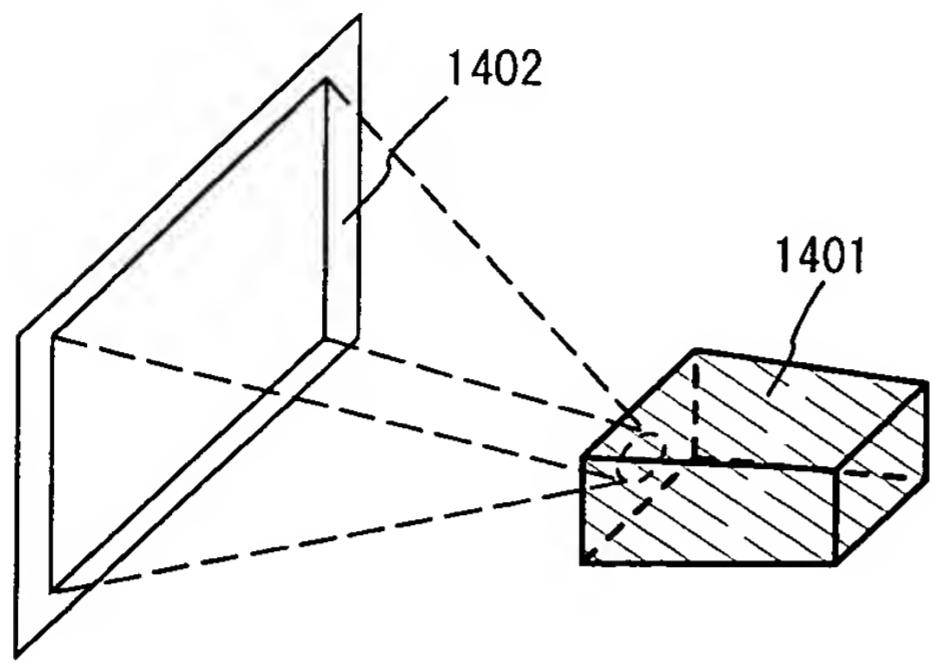


FIG. 15A

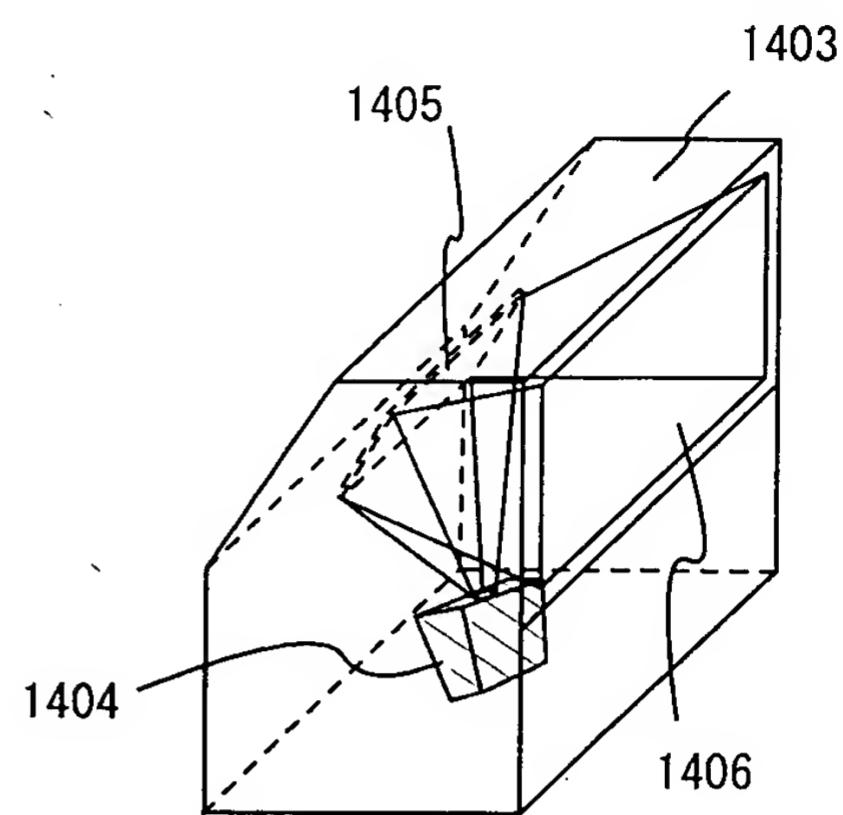


FIG. 15B

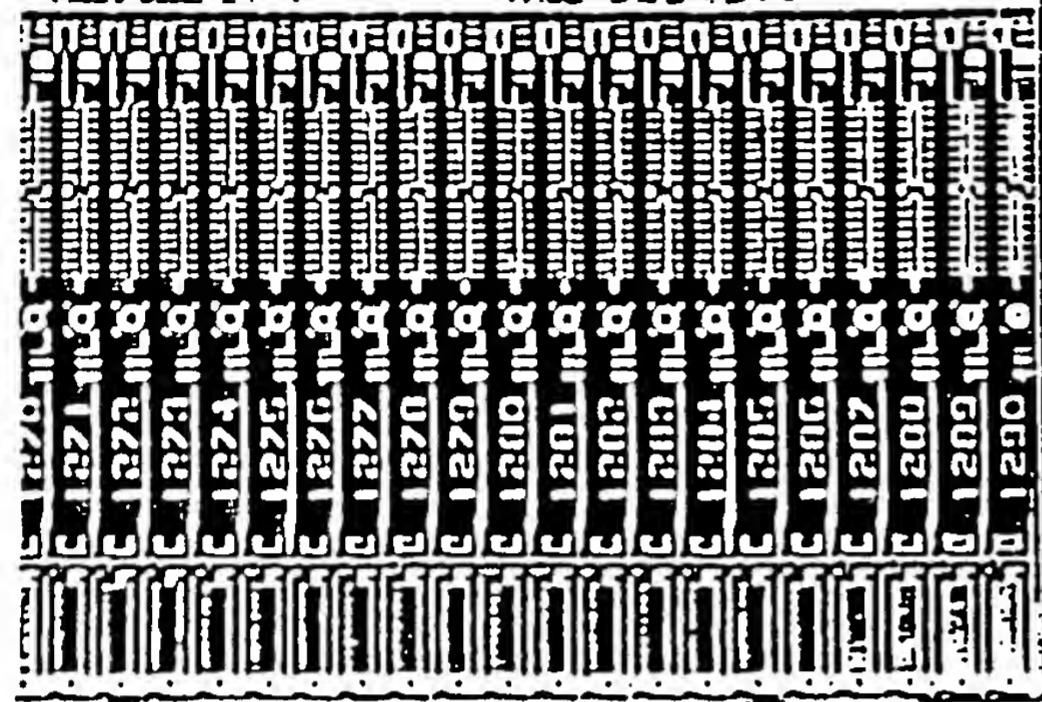
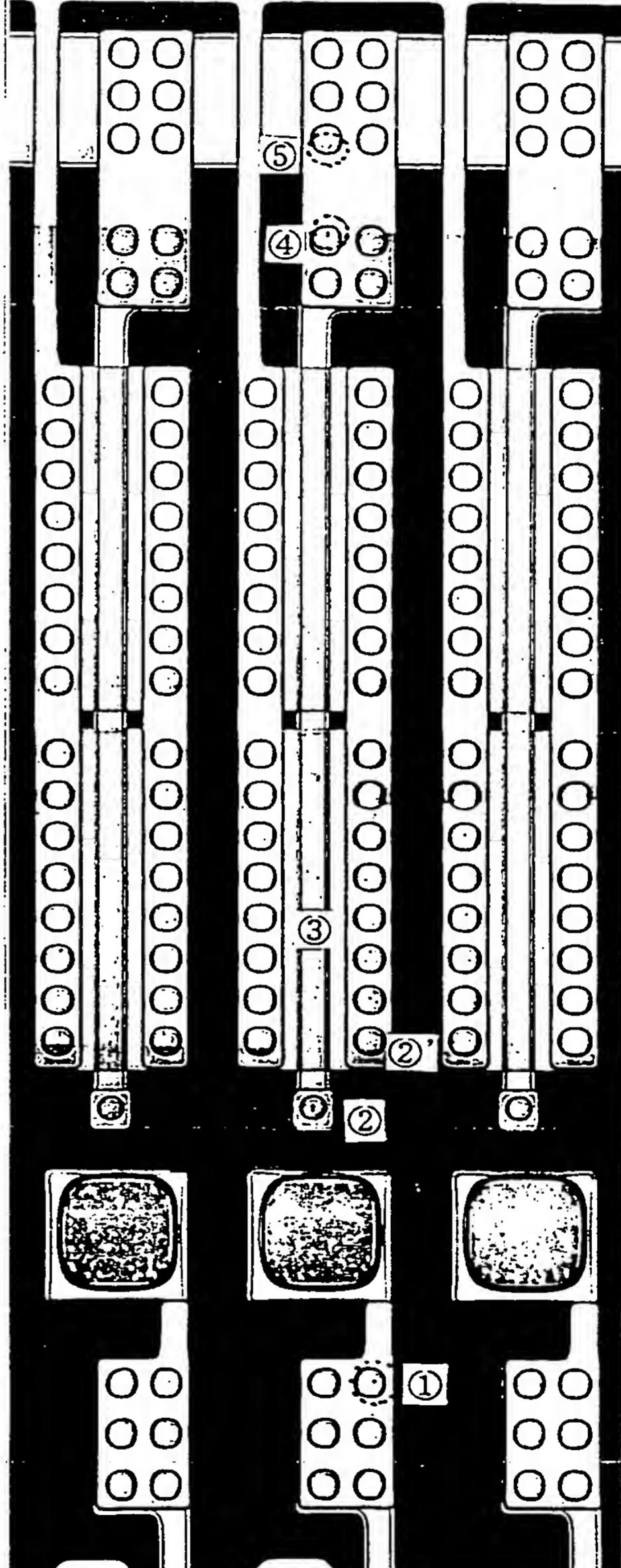


FIG. 16A



1279-th 1280-th 1281-th

FIG. 16B

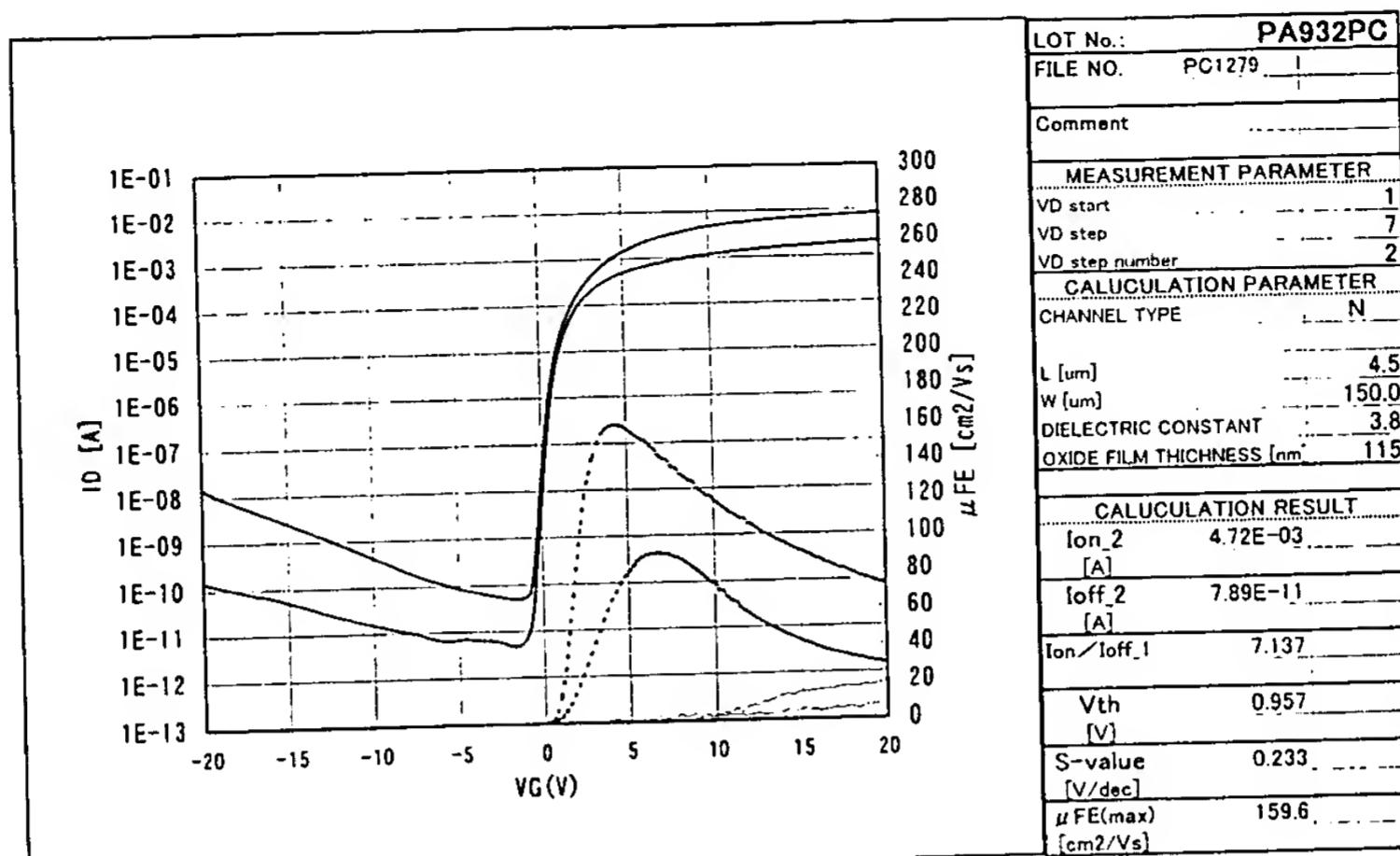


FIG. 17A

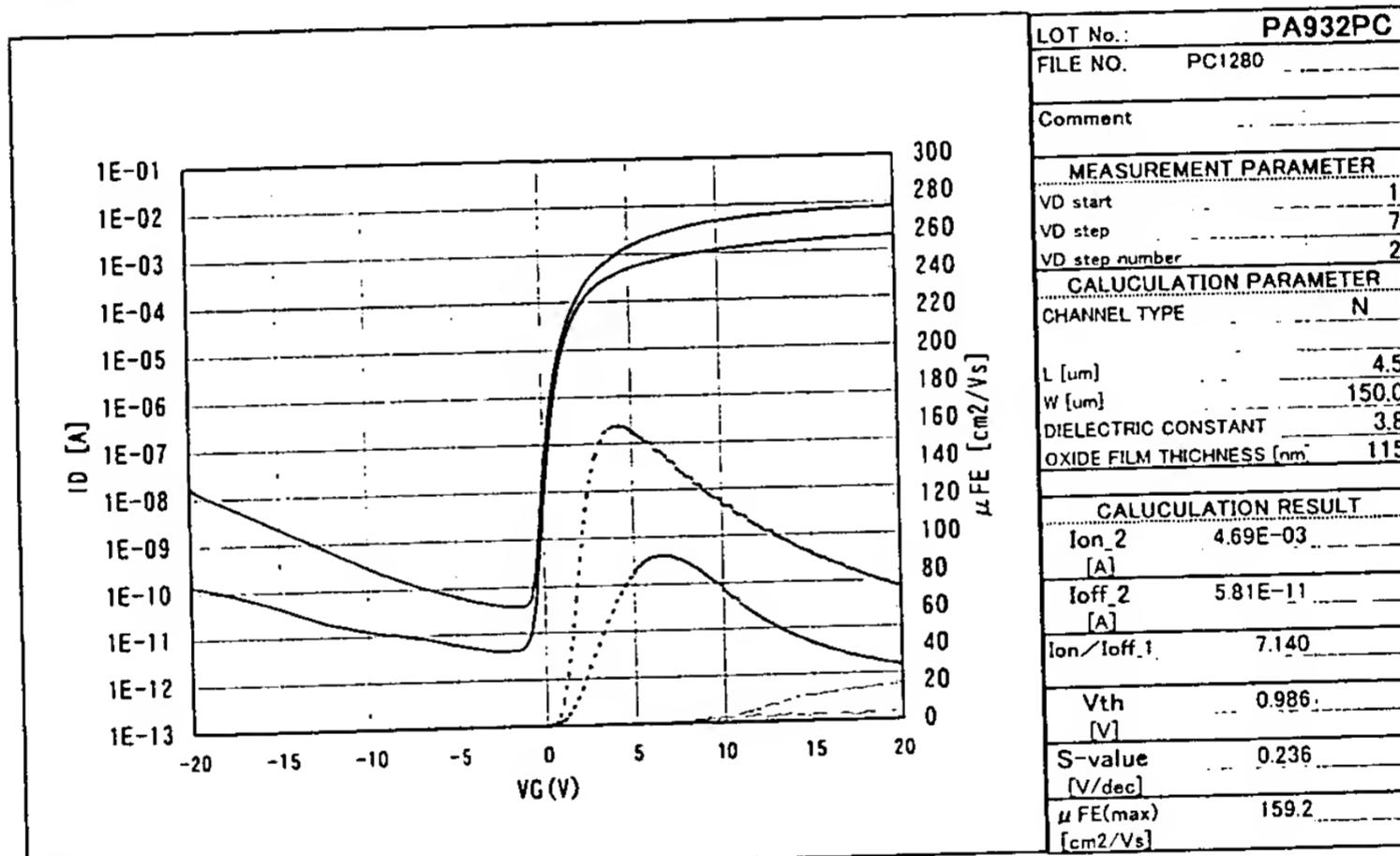


FIG. 17B

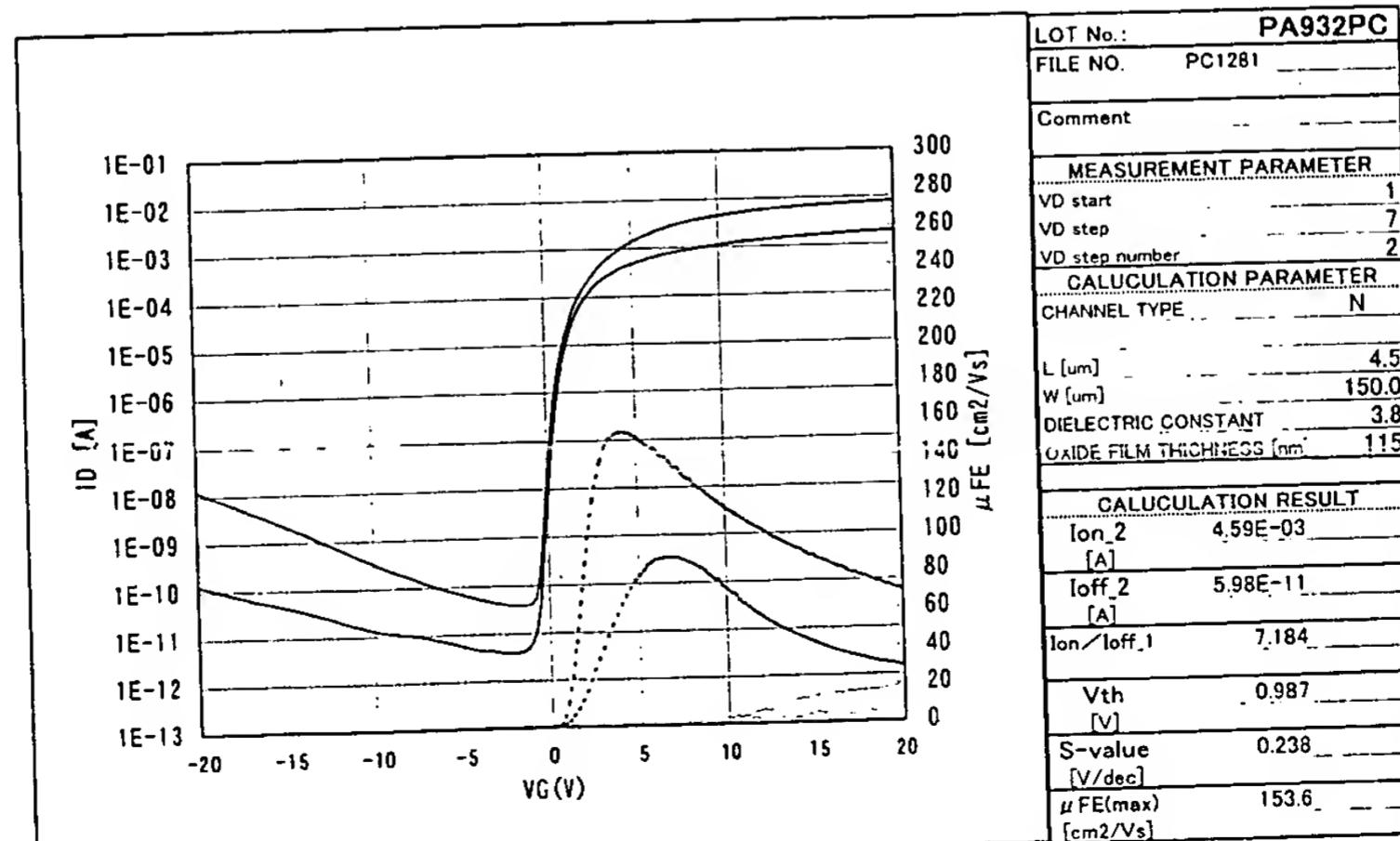


FIG. 17C

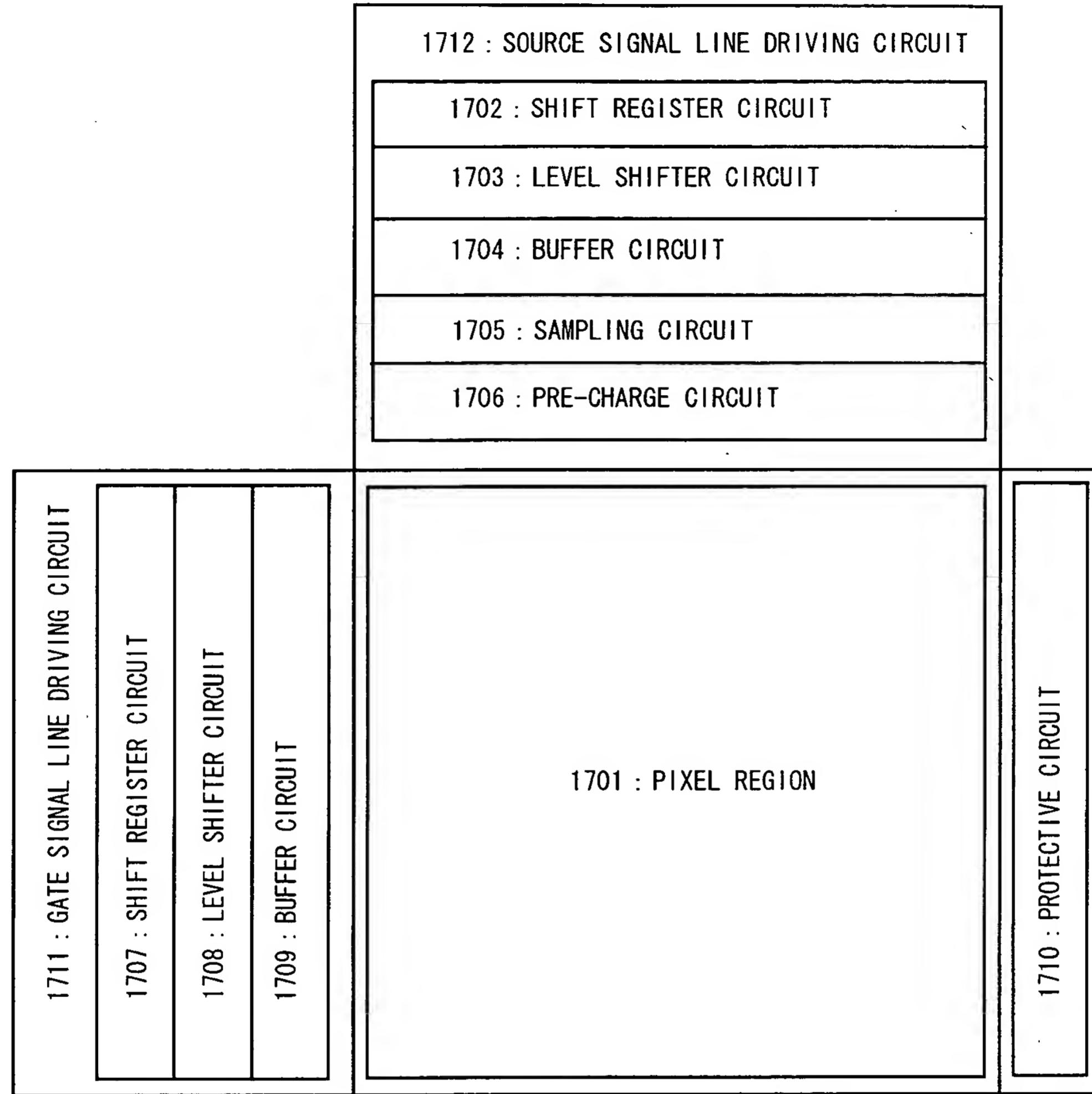


FIG. 18

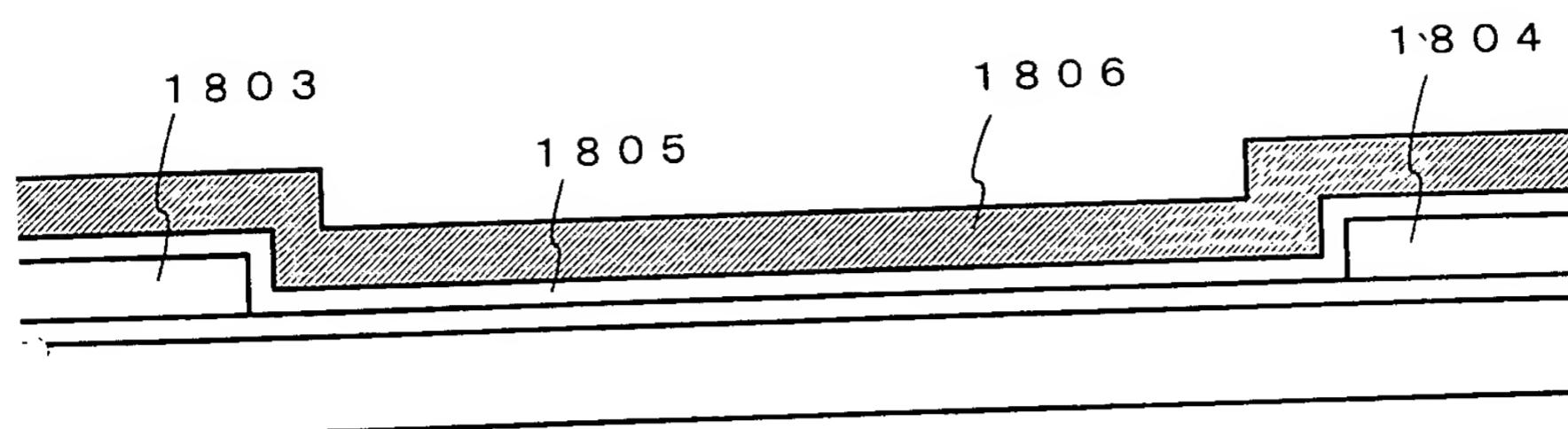


FIG. 19A

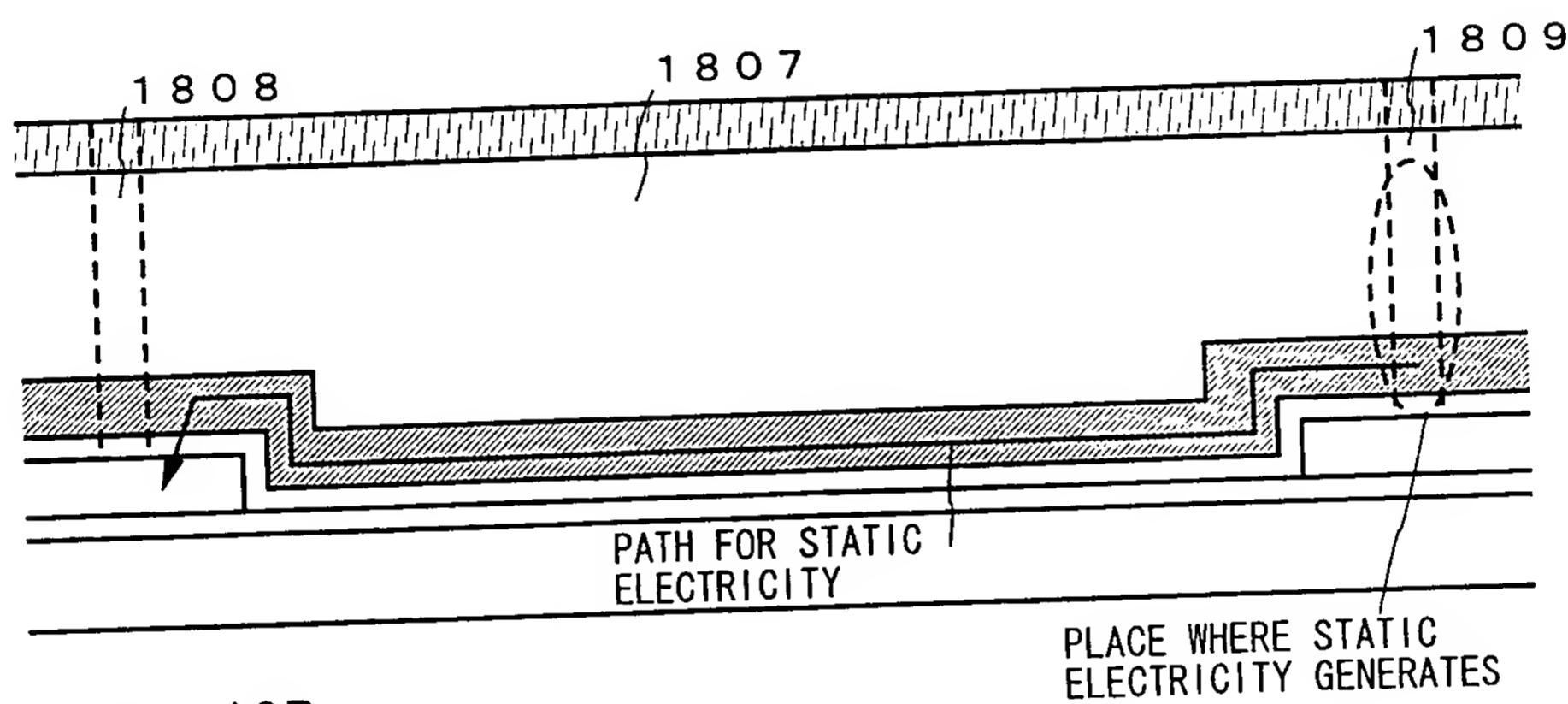


FIG. 19B

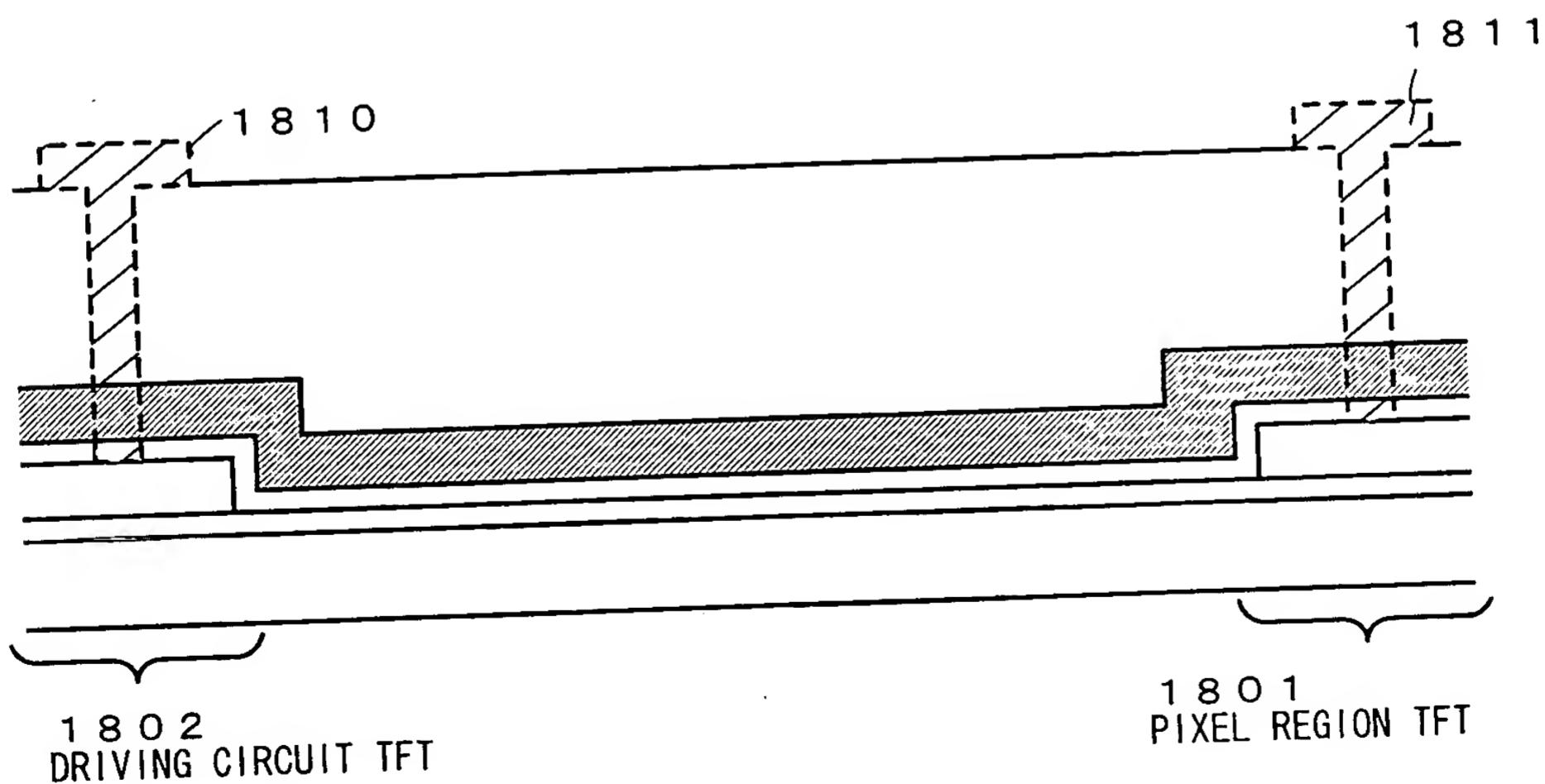


FIG. 19C

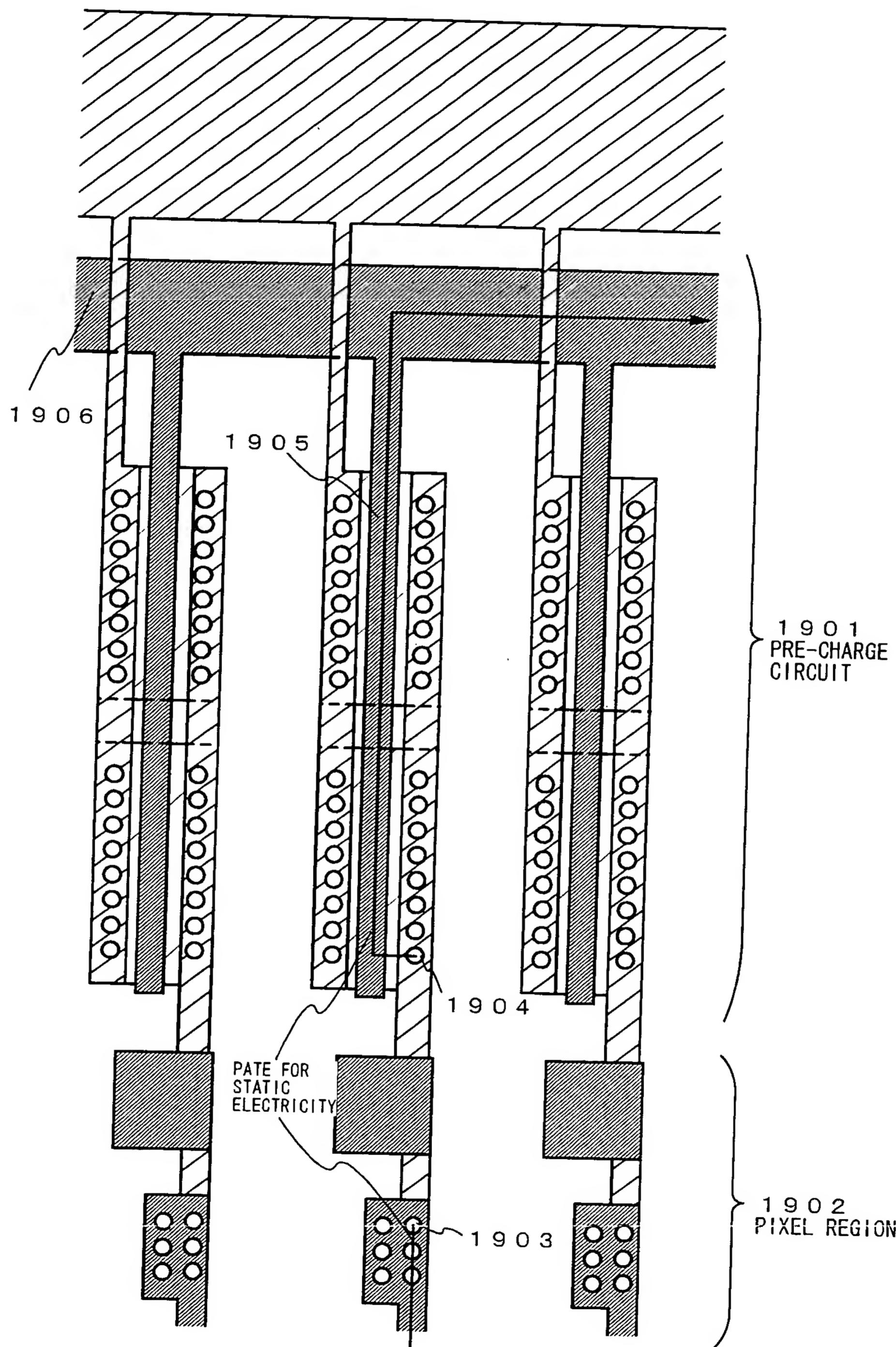


FIG. 20